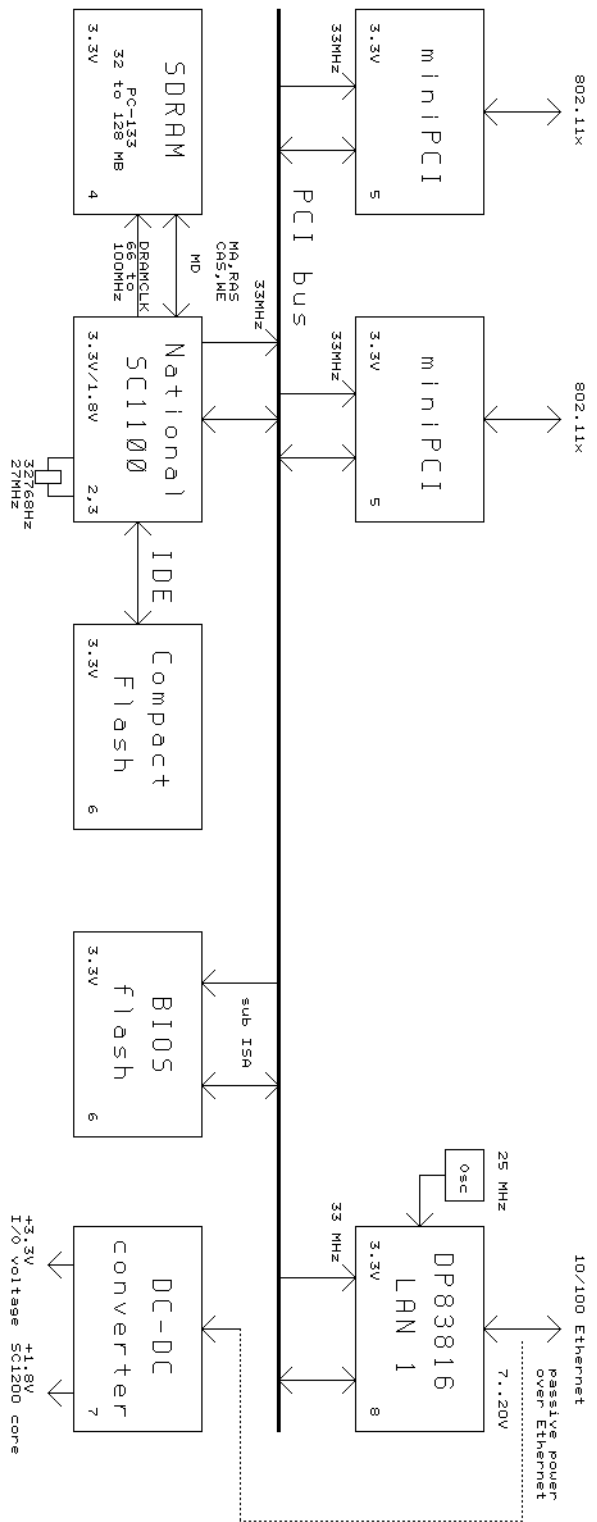


SC1100 wireless router platform

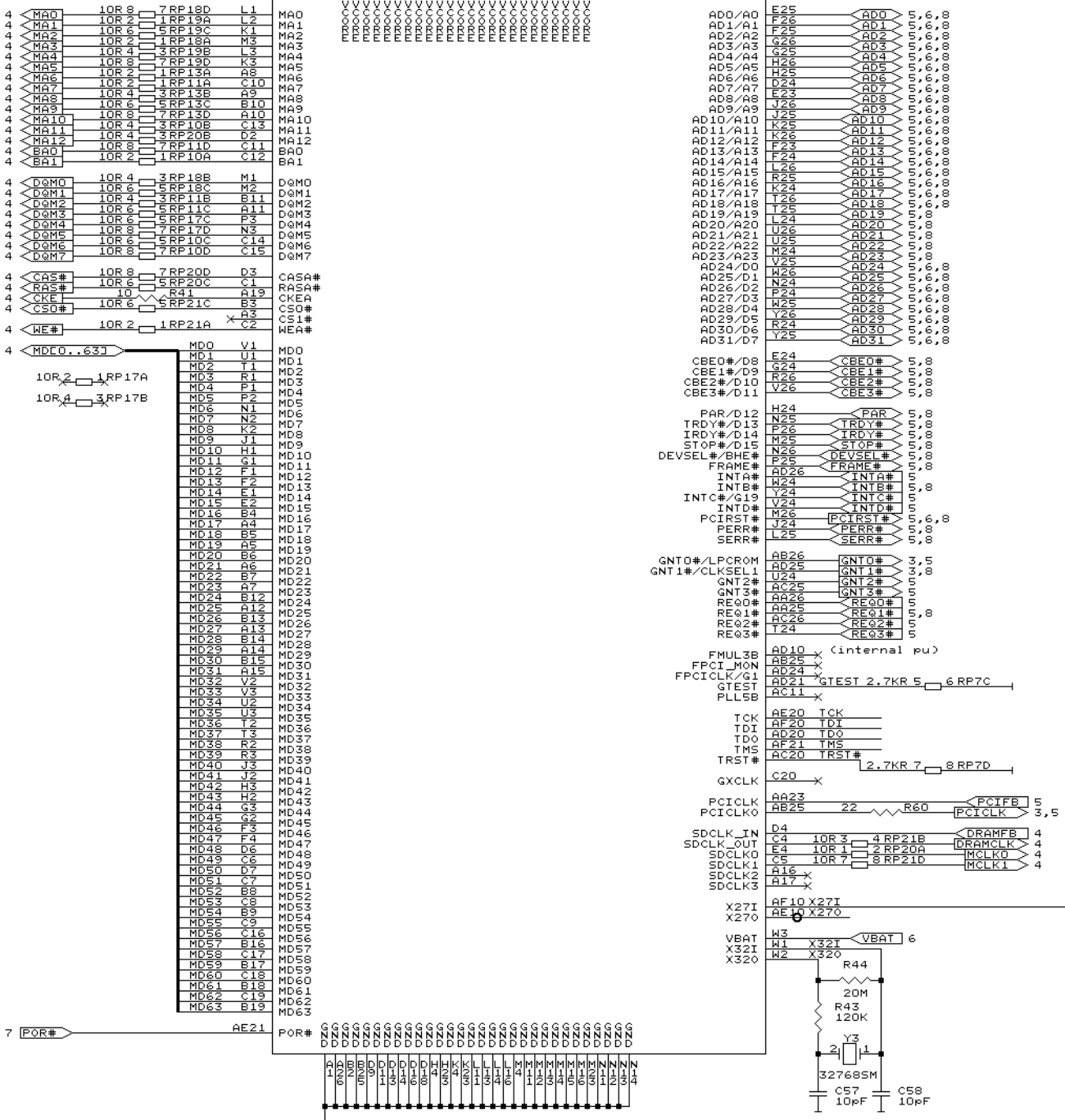


!LINK
!2.SCH
!3.SCH
!4.SCH
!5.SCH
!6.SCH
!7.SCH
!8.SCH

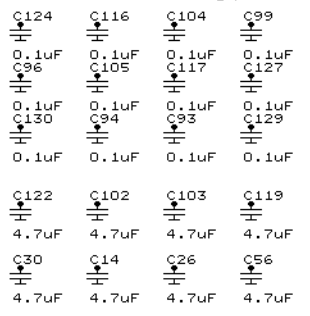
PC Engines GmbH	
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Title Block Diagram	
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Option A == 233 MHz CPU
 Option B == 266 MHz CPU

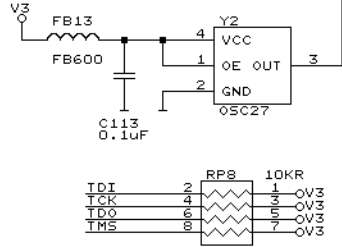
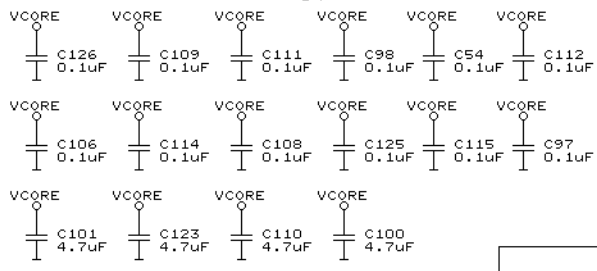
U10
 SC1100_A



V3 (VIO) bypass

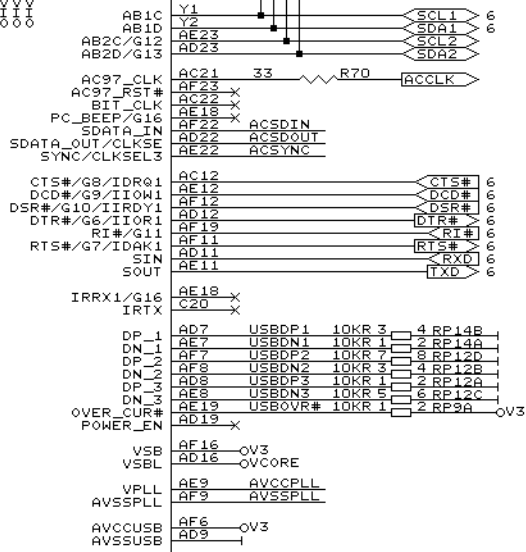
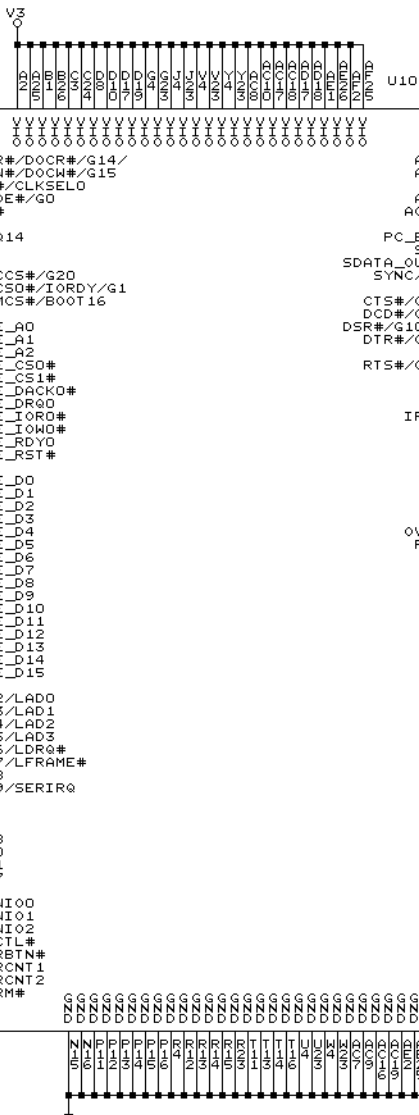


VCORE (1.8V) bypass

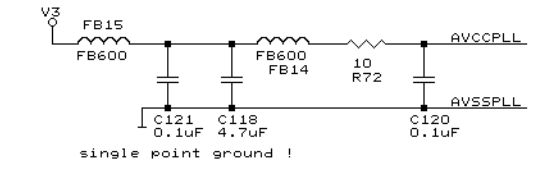
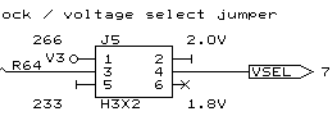
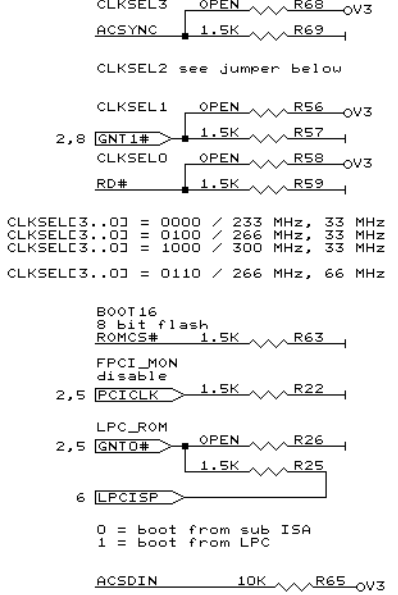


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Title National SC1100 CPU (1)			
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(DOCCS# p.u. in reference)
 (RD# p.u. in ref, but strap !)
 WR# 10K R67 OV3

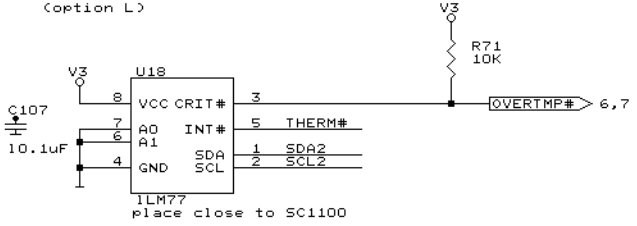


Strap options



Temperature sensor

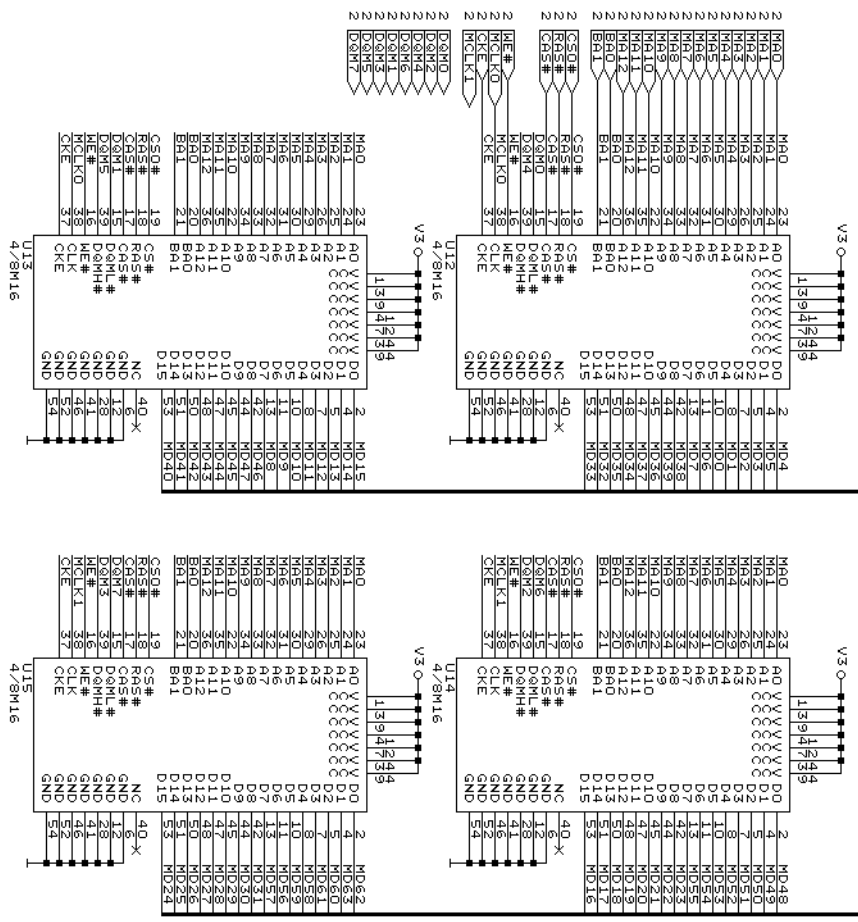
(option L)



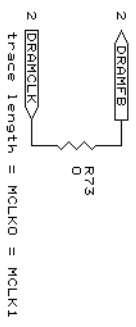
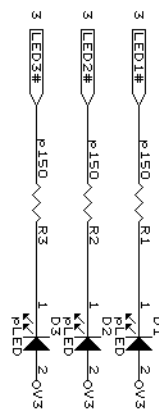
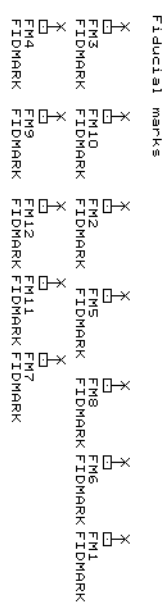
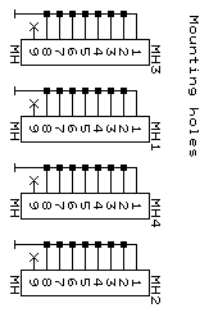
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CONFIDENTIAL			
Title National SC1100 CPU (2)			
Size	Document Number	WRAP	REV
A			2C
Date:	February 8, 2004	Sheet	3 of 8

Option C = 4Mx16 DRAM (32MB)
 Option D = 8Mx16 DRAM (64MB)
 Option E = 16Mx16 DRAM (128MB)

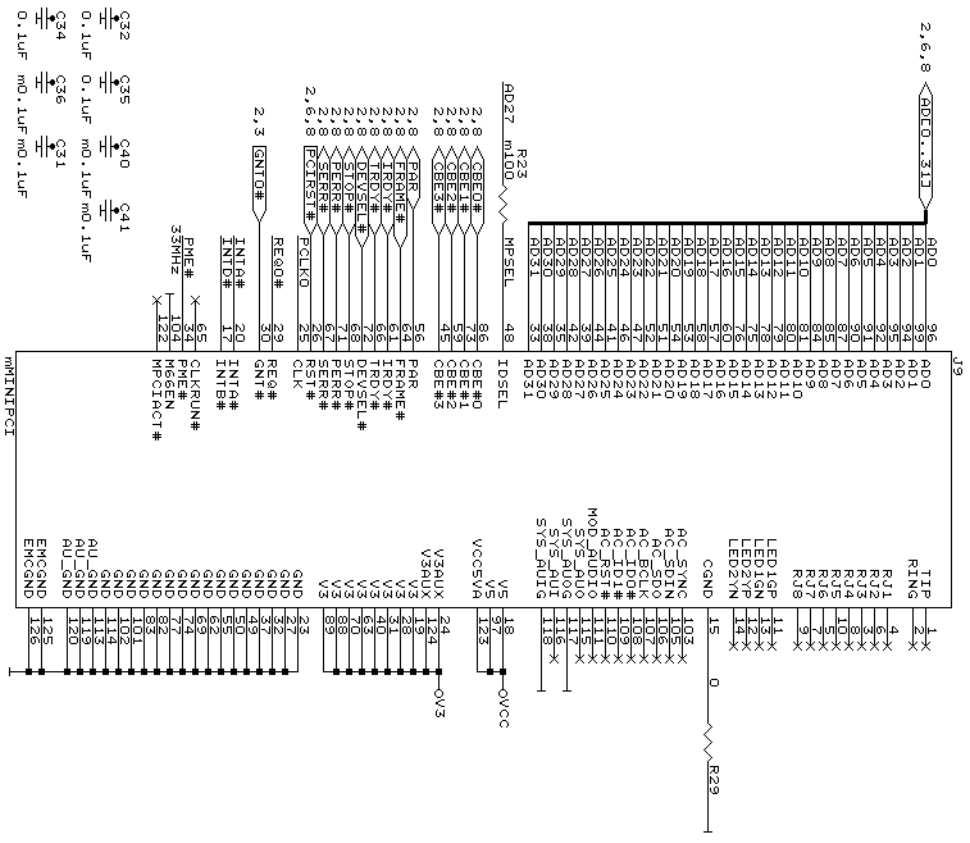
2 MCLK0..633



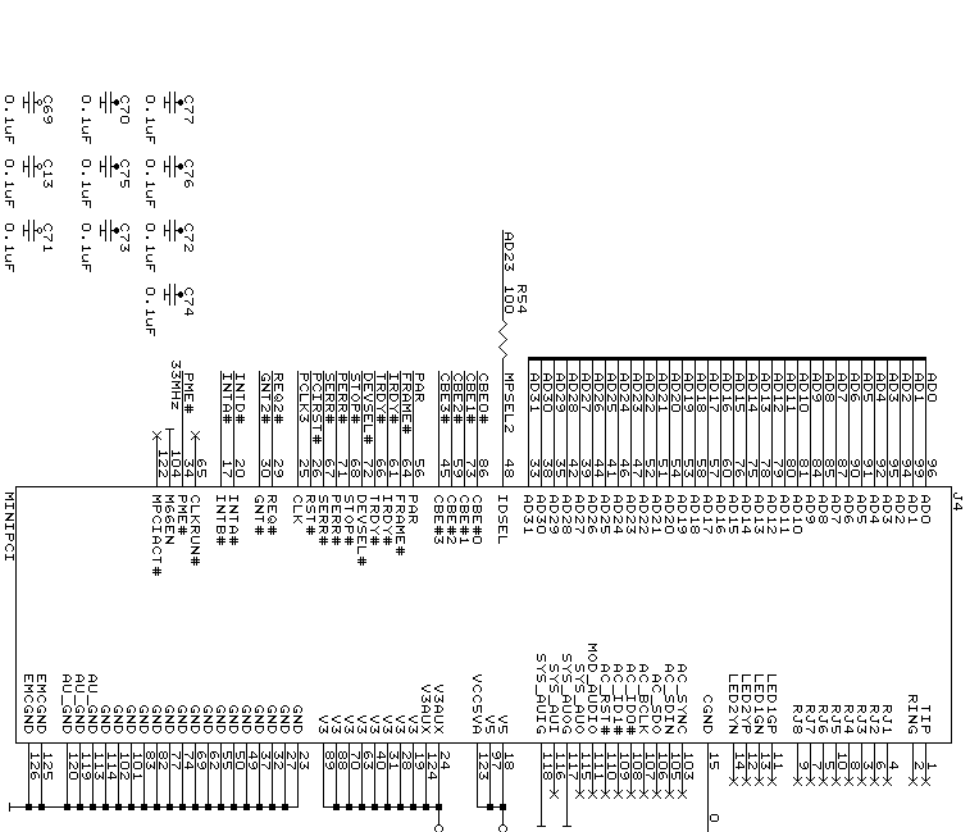
- Q141 Q138 Q135 Q137
- 0.1uF 0.1uF 0.1uF 0.1uF
- Q131 Q133 Q140 Q136
- 0.1uF 0.1uF 0.1uF 0.1uF
- Q134 Q132 Q139 Q142
- 0.1uF 0.1uF 0.1uF 0.1uF
- Q64 Q66 Q67 Q65
- 4.7uF 4.7uF 4.7uF 4.7uF



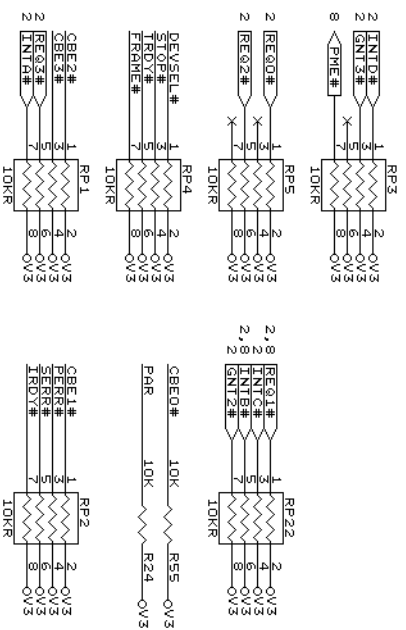
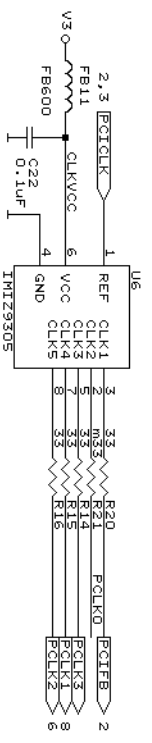
MINIPCI 2



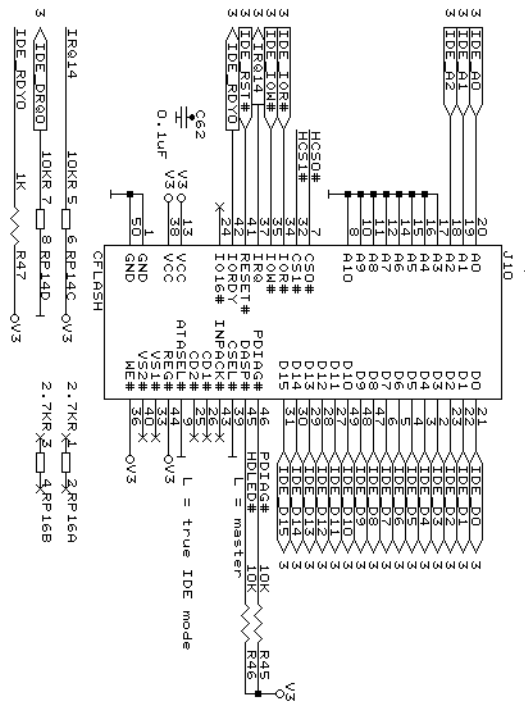
MINIPCI 1



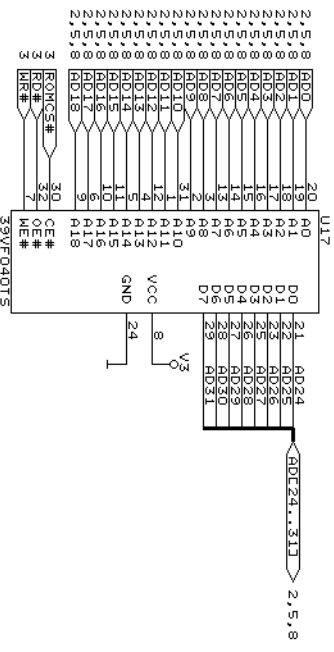
PCI clock buffer



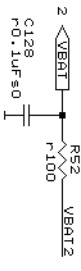
Compact flash



BIOS flash

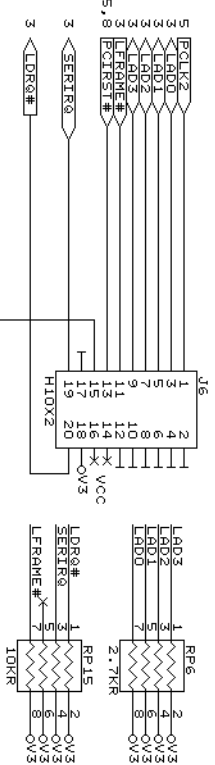


RTC power



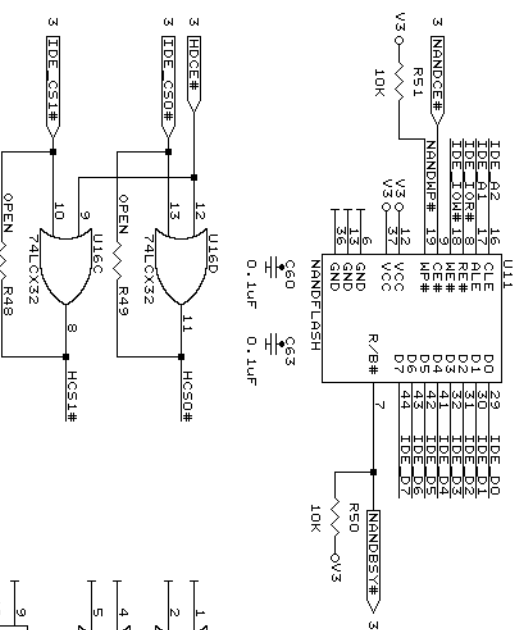
SC1100 draws up to 50mA load -
 suggest CR123A (1300mAh)
 OFR similar Lithium battery.
 VBAT shorted by 0 Ohm in v02 if A3 Rev. CPU

LPC expansion port

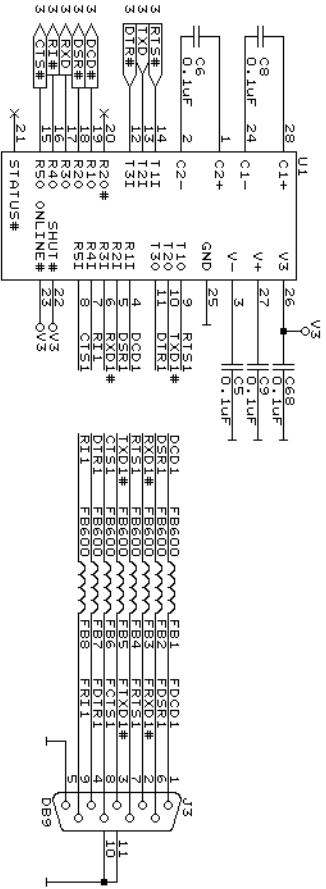


High = use LPC flash
 low = use on-board

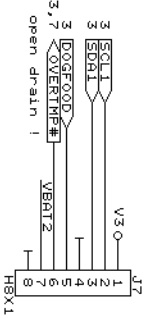
NAND flash



Console serial port

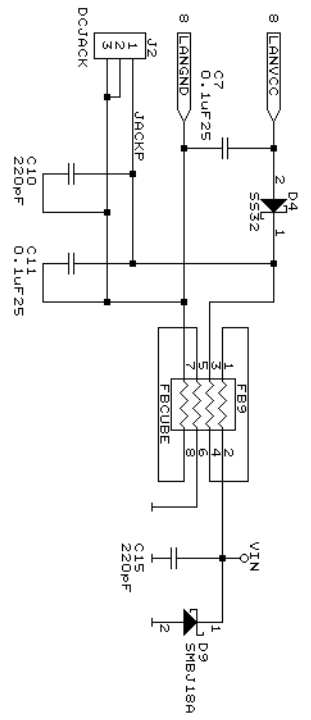


I2C / watchdog header

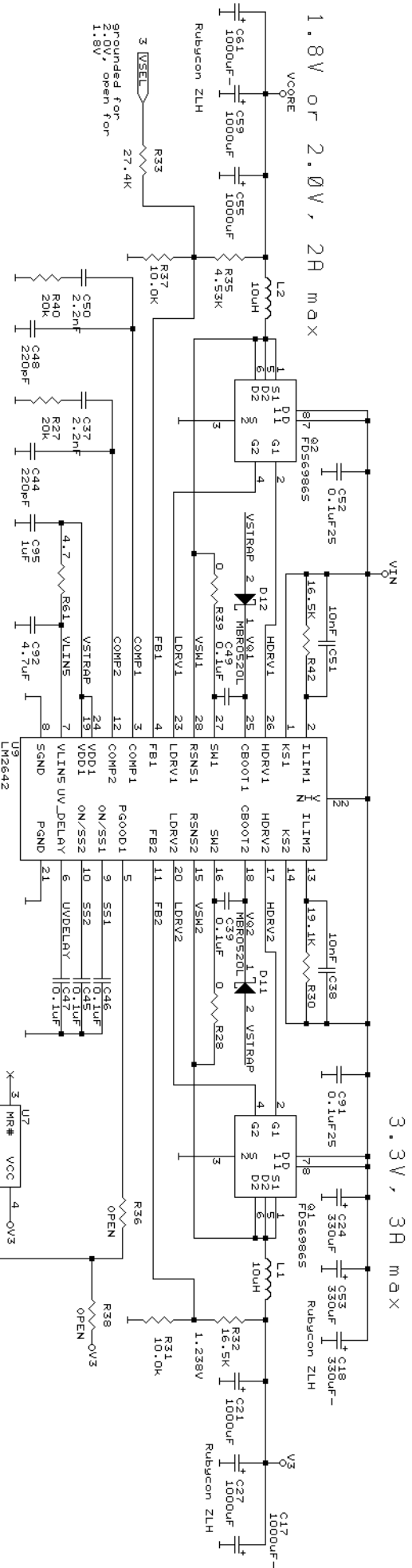


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Title	Flash, CF, serial port
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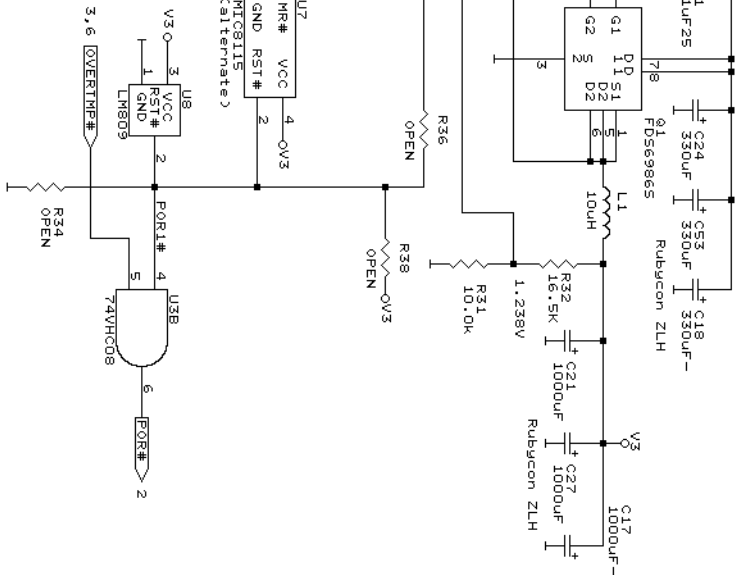
Power input
+7..20V



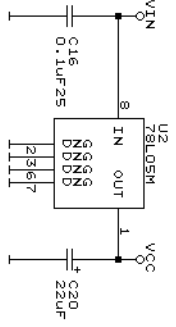
1.8V or 2.0V, 2R max



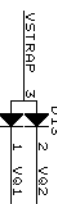
3.3V, 3R max



5V, 100mR max
Intended for bias, not supply !



alternative to MBR0520L
BMS40-06

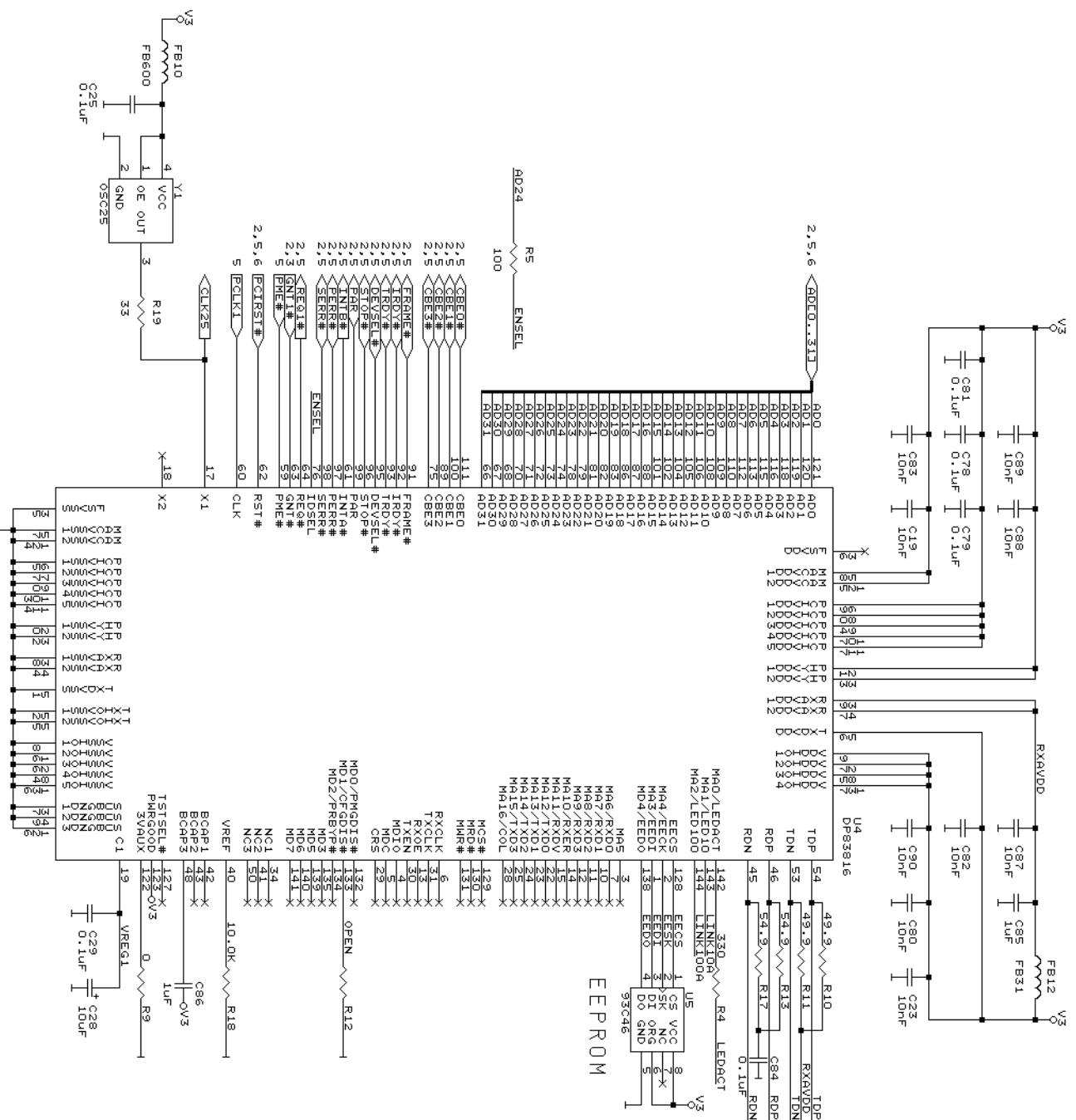


V3 3.3V discharge helps avoid reset problems

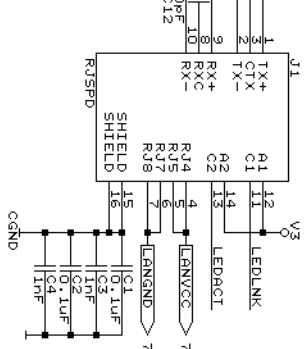
R7 150_1206

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DP83816 10/100 Ethernet



magnetics + RJ45



ESD diodes

