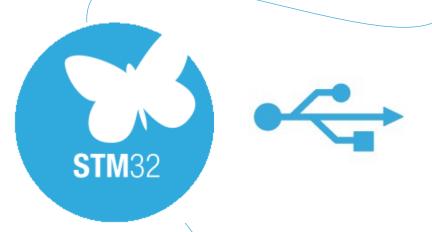


Roman Ludin

September 2014 Ver. 1.0





USB introduction 2

- Do you know what is USB?
 - It's easy, just check-out WIKI © http://en.wikipedia.org/wiki/USB

• Which are your customers USB projects?

• What are the key USB requirements of your customers?

• Who are the main competitors of ST and WHY?



the Universal Serial Bus

- The USB Universal Serial Bus is an industry standard developed in the mid-1990s that defines
 - Bus architecture
 - Cables, Connectors, Electrical levels
 - Communications protocols



- USB was designed to standardize the connection of computer peripherals
 - keyboards, pointing devices, digital cameras, printers, portable media players, disk drives and network adapters
- It has become common interface on other devices, such as smartphones, PDAs and video game consoles.
- USB has effectively **replaced** a variety of earlier interfaces, such as serial and parallel ports.



the Universal Serial Bus

Hot pluggable	YES
Protocol	Serial, pooled, host centric
Bitrate	1.5/ 12/ 480/ 5,000/ 10,000 Mbit/s
Max Length	5m
Max Voltage	5V
Max Current	0.5A general 5A charging device
Max Devices	127
Pins	4 1 supply, 2 data, 1 ground
Topology	Tired star





USB History 5



- The original USB 1.0 specification was introduced in January 1996
 - Defined data transfer rates of 1.5 Mbit/s" Low Speed" and 12 Mbit/s "Full Speed" The first widely used version of USB was **1.1**, was released in September 1998.
- The USB 2.0 specification was released in April 2000
 - Develop a higher data transfer rate achieving 480 Mbit/s
 - a 40-times increase over the original USB 1.1 specification
- The USB 3.0 specification was published on 12 November 2008.
 - Increase the data transfer rate (up to 5 Gbit/s)
 - decrease power consumption, increase power output
 - backwards-compatible with USB 2.0. USB 3.0 includes a new, higher speed bus called SuperSpeed in parallel with the USB 2.0 bus.
- The **USB 3.1** specification was released on 31 July 2013
 - Introducing a faster transfer mode called "SuperSpeed USB 10 Gbps"



www.usb.org

6

Universal Serial Bus Superspect Image: Search Image: S

1100	Detter	Delivery
I SR	POWPI	Tenver

- Wireless USB
- Hi-Speed USB

USB On-The-Go and Embedded Host

Tools

USB-IF eStore

Documents

USB-IF Compliance Program

USB FAQ

Events

Join USB-IF, Inc.

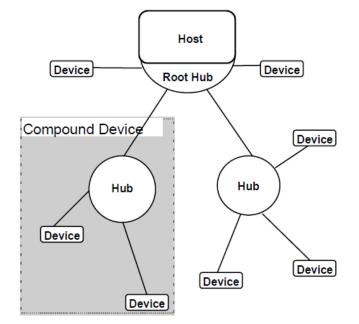
Resources

USB Implementers Forum, Inc.

- is a non-profit corporation that **developed** the USB **specification**
- The Forum facilitates the development of high-quality compatible USB peripherals (devices), and the quality of products that have passed compliance testing. Some of the many activities that the USB-IF supports include:
- USB Compliance Workshops and compliance test and tool development
- □ USB Developer Conferences
- Assignment of a **vendor ID**
- □ www.usb.org Web site
- □ and many more...

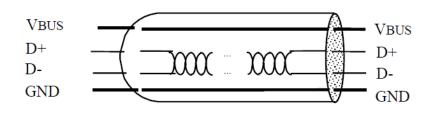
Bus Topology 7

- The USB physical interconnect is a **tiered star** topology.
- The USB connects USB devices with the USB host.
- A hub is at the center of each star. Each wire segment is a point-to-point connection.
- The maximum of **127 devices** can be connected in the bus
- The maximum of 5 hubs can be connected in series
- The maximum number of tiers allowed is seven
- The maximum cable length is 5meter





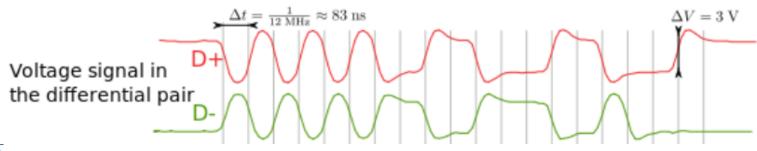
- USB is a serial bus, using four shielded wires for the USB 2.0 variant:
 - two for power (VBUS and GND),
 - two for differential data signals (D+ and D-).



Pin	Name	Wire color	Description
1	V _{BUS}	Red (or Orange)	+5 V
2	D-	White (or Gold)	Data-
3	D+	Green	Data+
4	GND	Black (or Blue)	Ground

USB 1.x/2.0 standard pinout

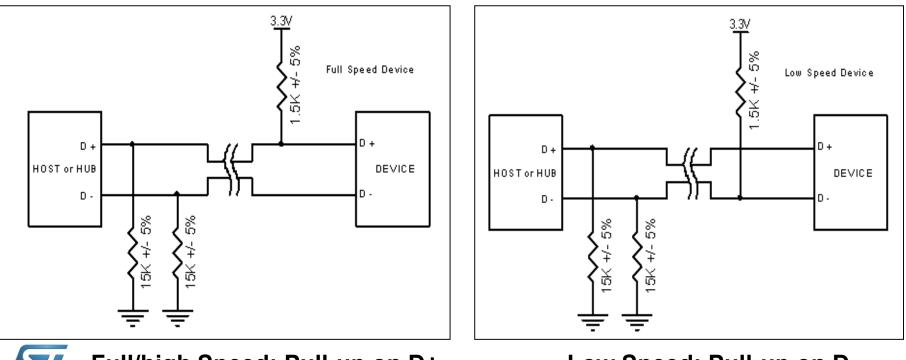
Non-Return-to-Zero Inverted (**NRZI**) encoding scheme is used for transferring data.





Electrical

- The 1.5K pull-up allows the host to detect the device attachment and its supported speed
- High-speed device is detected first as full-speed device then highspeed capability is detected through bus handshake mechanism called "chirp sequence"

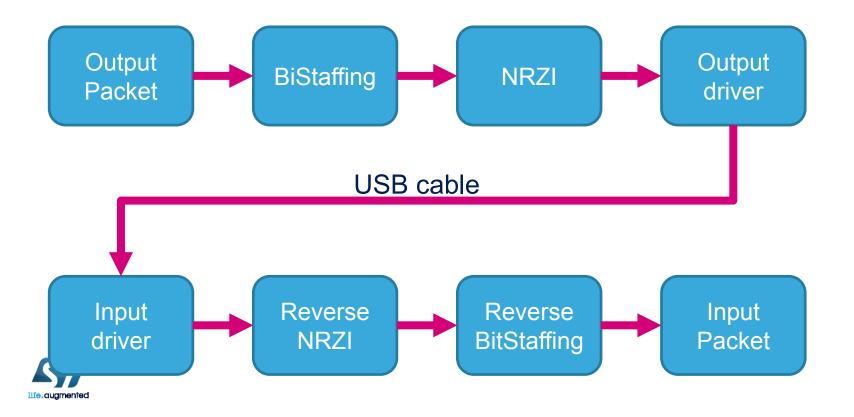


Full/high Speed: Pull-up on D+

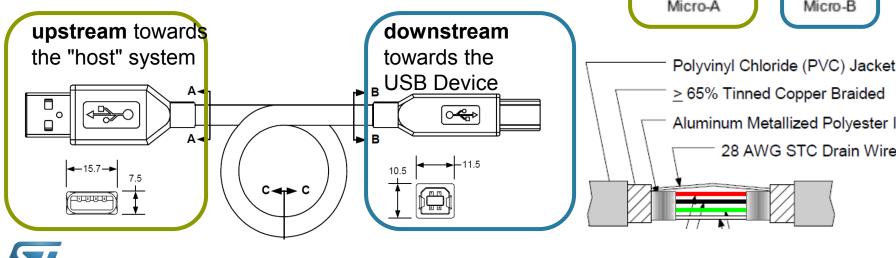
Low Speed: Pull-up on D-

Physical layer flow 10

- Packed is coded to NRZI with BitStaffing
- Then is send over differential bus

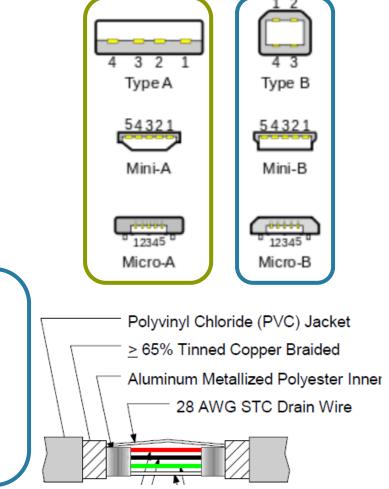


- USB specification provides the mechanical and electrical specifications for the cables, connectors
 - The USB physical topology consists of connecting the downstream hub port to the upstream port of another hub or to a device
- "keyed connector" are used to minimize end user termination problems



Mechanical 11

Standard, Mini, and Micro USB plugs.



USB over STM32 Family 12

MCU	Core	USB controller
STM32L0x2 STM32L0x3	Cortex-M0+	1x Crystal less USB 2.0 FS device with Link Power Managament (LPM) and Battery Charger detection (BCD)
STM32F0x2 STM32F0x8	Cortex-M0	1x Crystal less USB 2.0 FS device controller with Link Power Managment (LPM) and Battery Charger detection (BCD)
STM32L1	Cortex-M3	1x USB 2.0 FS device with internal 48 MHz PLL
STM32F102/103	Cortex-M3	1x USB 2.0 FS device controller
STM32F105/107	Cortex-M3	1x USB 2.0 FS device/host/OTG controller with on-chip PHY with 1.25 Kbytes of dedicated SRAM
STM32F2	Cortex-M3	1x USB 2.0 FS device/host/OTG controller with on-chip PHY 1x USB 2.0 FS/HS device/host/OTG controller with dedicated DMA, on-chip full-speed PHY and ULP
STM32F3	Cortex-M4	1x USB 2.0 FS device controller and LPM

STM32F4	Cortex-M4	1x USB 2.0 FS device/host/OTG controller with on-chip PHY 1x USB 2.0 FS/HS device/host/OTG controller with dedicated DMA, on-chip full-speed PHY and ULPI
		DMA, OI-Chip Iuli-speed I III and OLI I



STM32 on USB-IF integrators list _____

Universal Serial Bus ExpressCard. Search About USB-IF Channel Developers Members Home Press Products Product Search > Product Search > Search Catalog Items Product Search Search in These Results All Results for: STM32 STM32 Go New Search Select a Product 15 total results Page: 1 Results per page: 10 20 50 100 200 Product Name Company 🗘 Categories Туре STMicroelectronics Development > Peripheral Silicon > Low/Full STM32L053 Low/Full Speed Speed > Silicon Building Blocks STM32F103 Low/Full STMicroelectronics Development > Peripheral Silicon > Low/Full Speed > Silicon Building Blocks Speed STM32F205/7. Low/Full STMicroelectronics Development > Peripheral Silicon > Low/Full STM32F205 Speed Speed > Silicon Building Blocks STMicroelectronics Development > Peripheral Silicon > Low/Full STM32F405/7 Low/Full Speed > Silicon Building Blocks Speed STM32F207 Hi-Speed STMicroelectronics Development > Embedded Hosts > Hi-Speed > Other STM32F072 Low/Full STMicroelectronics Development > Peripheral Silicon > Low/Full Speed Speed > Silicon Building Blocks STMicroelectronics Development > Peripheral Silicon > Hi-Speed > STM32F205/7 Hi-Speed Silicon Building Blocks STM32F407 STMicroelectronics Development > Embedded Hosts > Hi-Speed > Hi-Speed Other STM32F105 Low/Full STMicroelectronics Development > Peripheral Silicon > Low/Full Speed Speed > Silicon Building Blocks STMicroelectronics Development > Peripheral Silicon > Hi-Speed > STM32F405/7 Hi-Speed Silicon Building Blocks STM32F407 STMicroelectronics Development > Embedded Hosts > Full Speed > Low/Full Speed Other STM32F303 Low/Full STMicroelectronics Development > Peripheral Silicon > Low/Full Speed Speed > Silicon Building Blocks STM32F373 Low/Full STMicroelectronics Development > Peripheral Silicon > Low/Full Speed > Silicon Building Blocks Speed Low/Full STMicroelectronics Development > Embedded Hosts > Full Speed > STM32F207 Other Speed STM32L152 Low/Full STMicroelectronics Development > Peripherals > Low/Full Speed > Speed Other

http://www.usb.org



USB VID/PID sublicensing service 14

Process & Schedule for PID request

- Request details: ٠
 - 1) COMPANY NAME AUTHORZING USE TO :
 - 2) Contact Name /Address and E-mail address:
 - 3) Name/Sales type of the STMicrocontroller product name :
 - 4) Name of USB end-product : { if possible USB device string Product}
- PID Booked in an internal ST Database
- By end of each quarter
 - ST send the approval list to the USB-IF
 - Approval by USB-IF
 - PID send to the customer with a "letter form Agreement"

MCD Support & Application > Request Request USB PIDs: N	
	OK Cancel
👔 Attach File	* indicates a required field
Company Name *	
Customer Contact Name *	
Customer E-mail address *	
Customer Full Address *	
Sales-Type of the Microcontroller	
Name of USB End-product *	
Qty/Year *	Production Quantity per Year
Production Start date *	
ST Requester Name *	8,/ LL
Request Date	3/26/2014
Status	Request •
PID	Please Do not fill this field



- Crystal-less* USB 2.0 FS interface (12Mbit/s)
 - Integrated on-chip 48 MHz oscillator with clock recovery system. No external resonator/ crystal needed (cost saving is in range of 0.10\$).
 - Up to 16 mono-directional or 8 bidirectional configurable endpoints •
 - Up to 1024 Bytes of dedicated packet buffer memory SRAM •
- Complies with *Link Power Management feature* (LPM) and *Battery* Charging Detection (BCD) specification 1.2
- Device Firmware Upgrade on the field over USB (boot loader)
- USB FS Device Library with intuitive USB device class drivers API
 - Examples and demo based on a set of 6 classes (Audio, CCID, CDC, HID, VCP, MSC).
 - Easy development of applications using USB full speed transfer types (control, interrupt, bulk and isochronous).
- Free PID/VID program for end-product certification



(USB) Clock recovery principle 16

- Provide the precise USB clock (48Mhz @ 0.25%) without any external resonator. It uses the USB Start-of-Frame (SOF) sent by a host at precise 1ms intervals (0.05% accurate), as a timing reference.
- SOF timing reference allows to automatically trim the int 48 MHz RC frequency based on the actual frequency error measured by a counter.
- HSI48 oscillator trimming step is 0.14% typical (0.2% max) to guarantee with a good margin the 0.25% accuracy needed for USB.
- Other synchronization sources (LSE, ext pin or SW trigger) works too.

Note : to calcul the precision of the output, +/-0.1% of error must be added on top of the reference signal precision. Ex : to reach 0.5% output, you need to have at worst 0.4% reference input.





T.O.M.A.S – Technically Oriented Microcontroller Application Services v0.01



USB HS 32

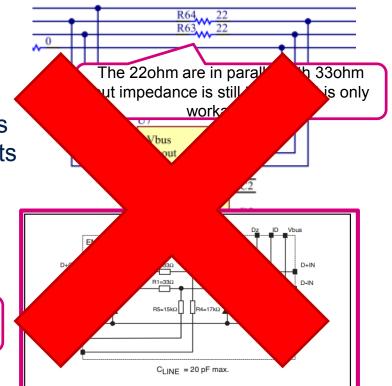
- STM32 for USB HS require the external PHY
- We recommend USB3300 which is tested with our devices and is also present on all our eval boards
- If the customer want to use different phy we recommend to test this new phy with STM32



USB Protections 33

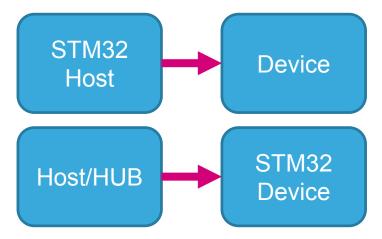
- STM32 FS USB connection
- Is not recommend use on DP and DN lines only 220hm resistors
- Maximum allowed resistance is 50hm with ESD protection
- Use ESD protections without internal resistors
- Otherwise you may not pass the validation
- The STM32 can work without external resistors on DP, DN lines it no specific ESD requirements is need than also without ESD protection



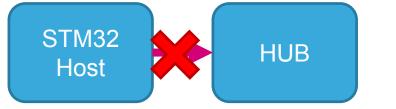


USB Library options 34

Connection supported by our library

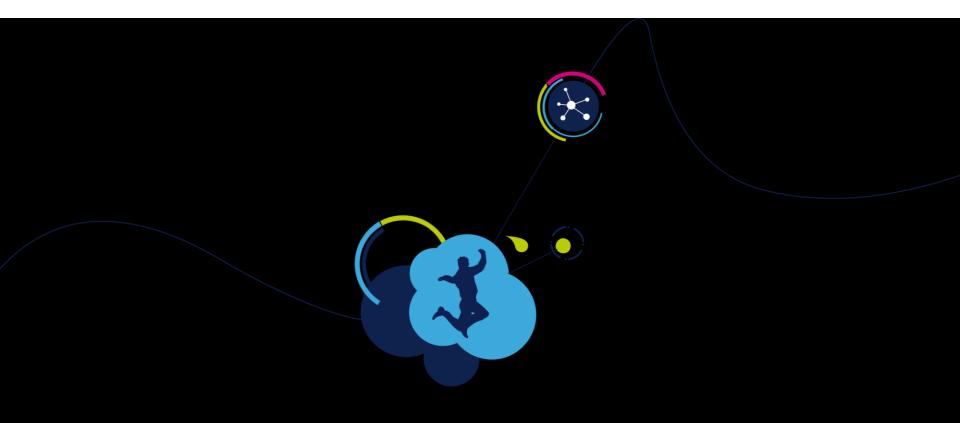


• Not supported configuration in ST USB library



Because ST USB HOST library not support HUB Class





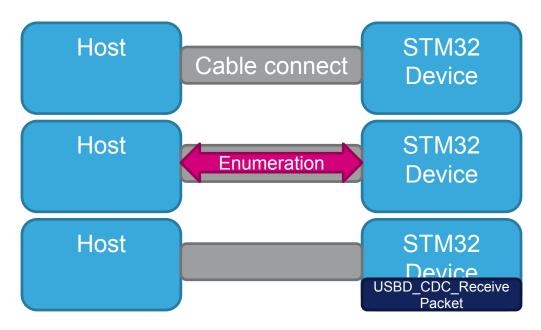
USB VCP Device with CubeMX



Cube VCP Functionality

• CDC FLOW 1/2

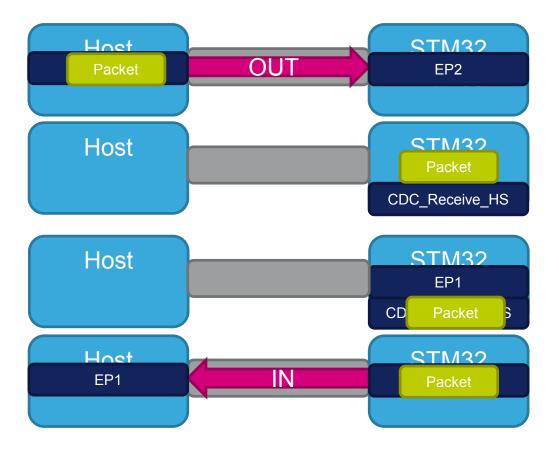
- Endpoint 0 by default
- Endpoint 1 bulk in
- Endpoint 2 bulk out
- Endpoint 3 Interrupt in(for control purposes)



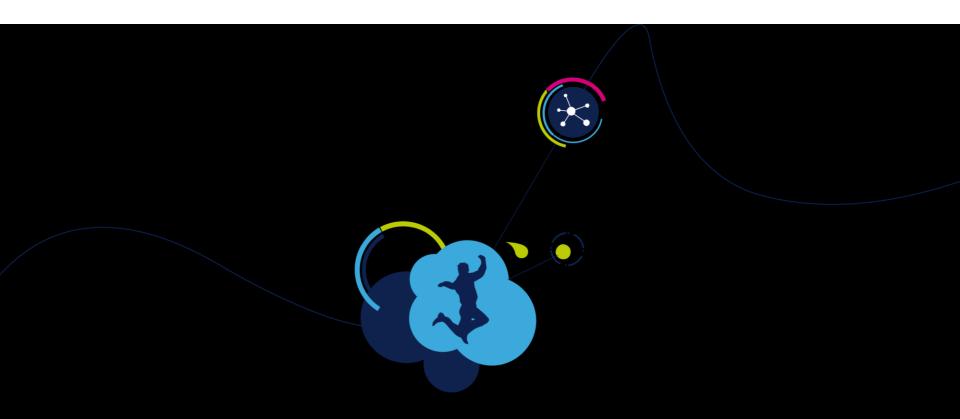


Cube VCP Functionality 45

• CDC FLOW 2/2







USB VCP Device L0 crystall less

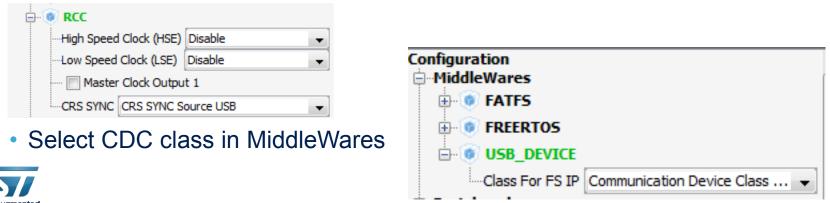




- Create project in CubeMX
 - Menu > File > New Project
 - Select STM32L0 > STM32L0x3 > LQFP64 > STM32L053R8Tx

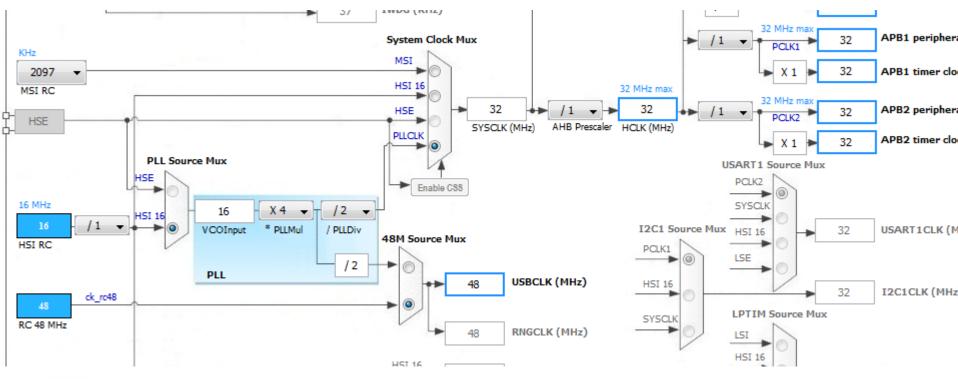


- Select RCC CRS SYNC to CRS SYNC Source USB
 - Because for crystal less device we need clock synchronization



Configure RCC clocks

- USBCLK source is RC48MHz
- Clock core to 32MHz from HIS PLL mul is 4x and divider 2x
- AHB/APB1/APB2 prescalers set to 1x



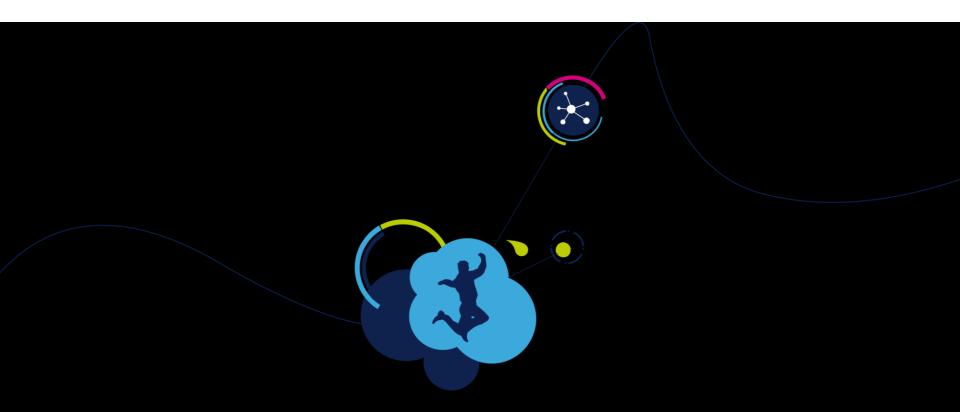


Now we set the project details for generation

- Menu > Project > Project Settings
- Set the project name
- Project location
- Type of toolchain
- Now we can Generate Code
 - Menu > Project > Generate Code

Project Settings		
roject Code Generator		
Project Settings		
Project Name		
L0_VCP		
Project Location		
D:\Radek\Training_examples\F4_USB		
Toolchain Folder Location		
D:\Radek\Training_examples\F4_USB\L0_VCP\		
Toolchain / IDE		
EWARM 7.20	-	
Mcu and Firmware Package Mcu Reference STM32L053R8Tx		
Firmware Package Name and Version		
STM32Cube FW_L0 V1.1.0		
	Ok	ncel





USB VCP Device F429 - Discovery





- Create project in CubeMX
 - Menu > File > New Project
 - Select STM32F4 > STM32F429/439 > LQFP144 > STM32F439ZITx
- Select USB HS OTG internal PHY(FS)

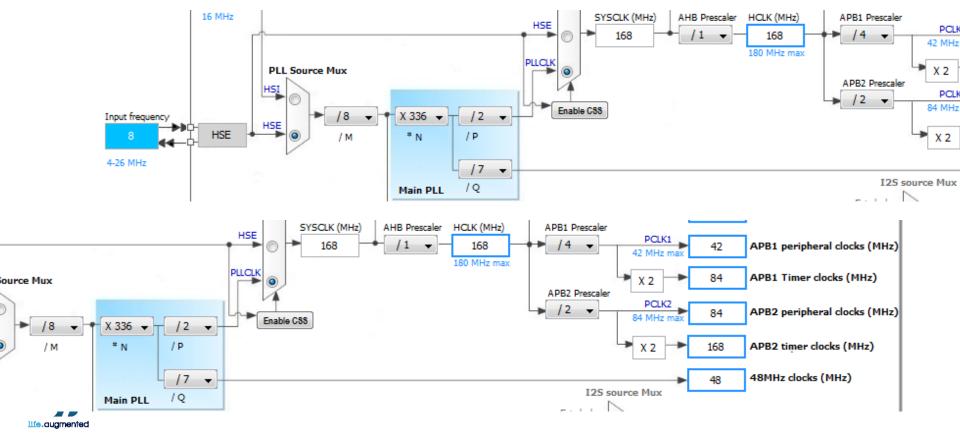


- Select HSE clock
 - (HSI cannot be used and STM32F4 have no clock synchronization)

Ė	Configuration
High Speed Clock (HSE) Crystal/Ceramic Resona 💌	i middleWares
Low Speed Clock (LSE) Disable	H 🗊 FATFS
Master Clock Output 1	E G FREERTOS
Master Clock Output 2	
Audio Clock Input (I2S_CKIN)	
Select CDC class in MiddleWares	
	Class For HS IP Communication Device Class (Vir 🗸
	Class For FS IP Disable

Configure RCC clocks

- For discovery kit set crystal frequency to 8MHz and M divider to 8x (1MHz)
- PLL set to N multiplier to 336x and P divider to 2x(168MHz 180 is not possible) and Q divider to 7x(48MHz)
- AHB prescaler to 1x, APB1 to 4x(42MHz) and APB2 to 2x(84MHz)



Now we set the project details for generation

- Menu > Project > Project Settings
- Set the project name
- Project location
- Type of toolchain
- Now we can Generate Code
 - Menu > Project > Generate Code

Project Settings		
oject Code Generator		
Project Settings		
Project Name		
F4_VCP_Device		
Project Location		
D:\Radek\Training_examples\F4_USB\abs		
Toolchain Folder Location		
D:\Radek\Training_examples\F4_USB\abs\F4_VCP_Device\		
Toolchain / IDE		
EWARM 6.70	-	
Mcu and Firmware Package		
Mcu Reference		
Mcu Reference STM32F439ZITx		
STM32F439ZITx		
STM32F439ZITx Firmware Package Name and Version		
STM32F439ZITx Firmware Package Name and Version		
STM32F439ZITx Firmware Package Name and Version		
STM32F439ZITx Firmware Package Name and Version		
STM32F439ZITx Firmware Package Name and Version		
STM32F439ZITx Firmware Package Name and Version	Ok	Cancel



- CubeMX will generate for you whole project
- For Keil is necessary in startup_stm32xxxx.s increase heap otherwise USB will be not functional(0x200 heap is to low for USB)

; <h> Heap Configuration ; <o> Heap Size (in Bytes) <0x0-0xFFFFFFF:8> ; </h>

Heap_Size EQU 0x00000200

• Change it to:

; <h> Heap Configuration ; <o> Heap Size (in Bytes) <0x0-0xFFFFFFF:8>

```
; </h>
```

Heap_Size EQU 0x0000800

Then USB device will be successful enumerated



- How send receive data over VCP
- Function which handle VCP operation are in generated file usbd cdc if.c
- APP RX DATA SIZE and APP TX DATA SIZE define size of sending and receiving buffers

```
/* USER CODE BEGIN 1 */
/* Define size for the receive and transmit buffer over CDC */
/* It's up to user to redefine and/or remove those define */
#define APP RX DATA SIZE 64
#define APP TX DATA SIZE 64
 /* USER CODE END 1 */
```

 Callback from control interface which allow to send COM port parameters Is used only if you really want to send data over COM port(UART)

```
static int8 t CDC Control FS (uint8 t cmd, uint8 t* pbuf, uint16 t length)
```



- Receive callback function
- In case you wand to receive more bytes you must call USBD CDC ReceivePacket(hUsbDevice 0);
- Otherwise the USB will not accept any data until you call this function

```
static int8 t CDC Receive FS (uint8 t* Buf, uint32 t *Len)
{
 /* USER CODE BEGIN 7 */
 USBD_CDC_ReceivePacket(hUsbDevice_0);
 return (USBD OK);
  /* USER CODE END 7 */
}
```



- The Windows terminals using CDC commands to set correct line coding
- But they also want to read this coding back
- For this purpose we need to handle this actions
- This actions are done throe function:

static int8 t CDC Control FS (uint8 t cmd, uint8 t* pbuf, uint16 t length)

 We use simply trick, we create buffer where we store this information from PC and the we can send them back

```
uint8_t tempbuf[6];
 /* USER CODE END 3 */
```



This part in CDC_Control_FS handling the storing and riding part form buffer

```
case CDC SET_LINE_CODING:
       tempbuf[0]=pbuf[0];
       tempbuf[1]=pbuf[1];
       tempbuf[2]=pbuf[2];
       tempbuf[3]=pbuf[3];
       tempbuf[4]=pbuf[4];
       tempbuf[5]=pbuf[5];
       tempbuf[6]=pbuf[6];
   break;
```

```
case CDC GET LINE CODING:
      pbuf[0]=tempbuf[0];
      pbuf[1]=tempbuf[1];
      pbuf[2]=tempbuf[2];
      pbuf[3]=tempbuf[3];
      pbuf[4]=tempbuf[4];
      pbuf[5]=tempbuf[5];
      pbuf[6]=tempbuf[6];
  break;
```

Now will be communication with PC functional



- This function you need to call if you want to send data over VCP
- In CubeMX 4.6 wrong USBD_CDC_SetTxBuffer Buffer parameter, please correct it as bellow

```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
{
 uint8_t result = USBD_OK;
 /* USER CODE BEGIN 8 */
 USBD CDC SetTxBuffer(hUsbDevice 0, Buf, Len);
 result = USBD CDC TransmitPacket(hUsbDevice 0);
 /* USER CODE END 8 */
 return result;
}
                                                                Example of wrong
                                                                 generated code
uint8 t CDC Transmit HS(uint8 t* Buf, uint16 t Len)
{
 uint8 t result = USBD OK;
 /* USER CODE BEGIN 13 */
 USBD_CDC_SetTxBuffer(hUsbDevice_1, UserTxBufferHS, Len);
 result = USBD CDC TransmitPacket(hUsbDevice 1);
                                                       Irelevant buffer change it to
 /* USER CODE END 13 */
 return result;
                                                     'Buf' or store your data into this
                                                                  buffer
```

- If you want send lot of data with function CDC Transmit FS and you want to rewrite his buffer you must check first if the periphery release this buffer
- For this you need check the state of CDCUSBhandle something like this

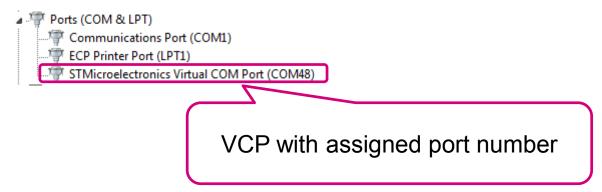
```
if(((USBD CDC HandleTypeDef*)(hUsbDeviceFS.pClassData))->TxState==0){
   CDC Transmit FS(buffer, length);
 }
```

 The function first check if USB IN(Tx) is complete and allow to use transmit function

 Correct handling of transmit complete is use USBD CDC DataIn callback in usbd_cdc.c and implement callback to user application Unfortunately for this is necessary change library files!!



- Because Windows can select for VCP very high com port number you need the terminal where you can select the com number
- For example: <u>http://realterm.sourceforge.net/</u>
- If the USB is connected to PC it must be displayed in Device Manager



 In case you have no driver for VCP download it from: <u>http://www.st.com/web/en/catalog/tools/FM147/CL1794/SC961/SS1533/PF25</u> <u>7938?s_searchtype=keyword</u>



Simple Loopback only for testing!!!

```
static int8_t CDC_Receive_FS (uint8_t* Buf, uint32_t *Len)
{
 /* USER CODE BEGIN 7 */
 CDC Transmit FS(Buf,*Len);
 USBD CDC ReceivePacket(hUsbDevice 0);
 return (USBD OK);
  /* USER CODE END 7 */
}
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
{
 uint8 t result = USBD OK;
 /* USER CODE BEGIN 8 */
 USBD CDC SetTxBuffer(hUsbDevice 0, Buf, Len);
 result = USBD CDC TransmitPacket(hUsbDevice 0);
 /* USER CODE END 8 */
 return result;
}
```



```
    Transmit will be still same
```

```
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
{
    uint8_t result = USBD_OK;
    /* USER CODE BEGIN 8 */
    USBD_CDC_SetTxBuffer(hUsbDevice_0, Buf, Len);
    result = USBD_CDC_TransmitPacket(hUsbDevice_0);
    /* USER CODE END 8 */
    return result;
}
```



Chapter 5.8.3

- Communication over VCP with Windows is specific
- There is one problematic part which is not obvious
- The Windows require for end of in transfer packet smaller then maximum size or zero length packet
- If this condition is not meet you will never see data in your application!!!!
 USB specification 2.0

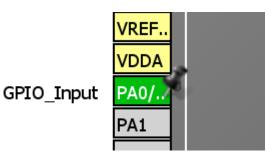
contain the remaining data. A bulk transfer is complete when the endpoint does one of the following:

- Has transferred exactly the amount of data expected
- Transfers a packet with a payload size less than wMaxPacketSize or transfers a zero-length packet

life.augmented

Windows use in VCP this condition as end of transfer

- In CubeMX add PA0(Button) pin as input
- It will help with problem demonstration and protect terminal from spamming



And regenerate code



```
    Corrected transmit function(usbf cdc if.c)

uint8 t CDC Transmit_HS(uint8_t* Buf, uint16_t Len)
{
  uint8 t result = USBD OK;
  /* USER CODE BEGIN 13 */
 USBD CDC SetTxBuffer(hUsbDevice 1, Buf, Len);
  result = USBD CDC TransmitPacket(hUsbDevice 1);
  /* USER CODE END 13 */
 return result;
}
```

We don't need to do anything with receive

```
static int8 t CDC Receive HS (uint8 t* Buf, uint32 t *Len)
{
 /* USER CODE BEGIN 12 */
 return (USBD OK);
  /* USER CODE END 12 */
}
```



 Include the usbd_cdc_if.h into main.c this allow us to use Transmit function

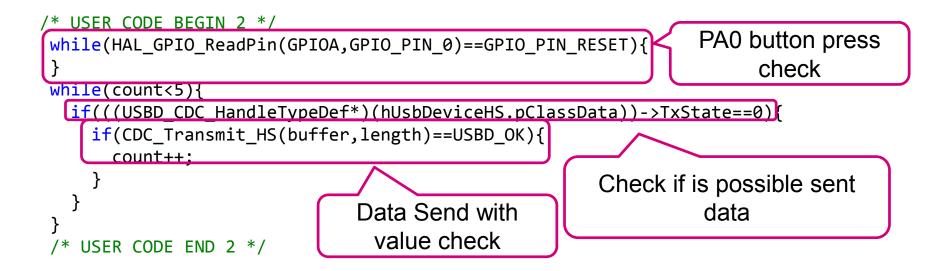
```
/* USER CODE BEGIN Includes */
#include "usbd_cdc_if.h"
/* USER CODE END Includes */
```

 Create buffer and buffer length variable and variable for loop limiting purpose, define extern USB handle(only for OTG devices)

```
/* USER CODE BEGIN PFP */
uint8_t buffer[64];
uint8_t length=64;
uint8_t count=0;
extern USBD_HandleTypeDef hUsbDeviceHS;
/* USER CODE END PFP */
```



- We will wait on PA0 button press
- After that program sent 5x buffer 64byte length
- But on windows terminal we not get any data



• Try to decrease length variable to for example to 63



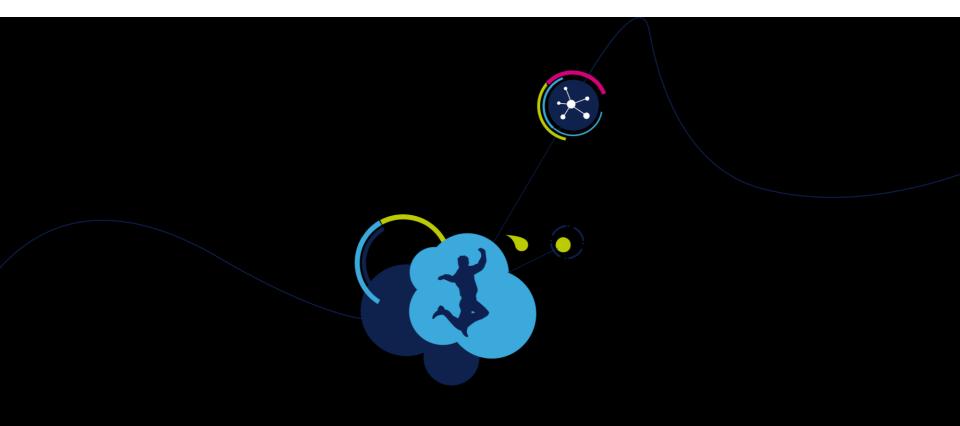
Check if is possible send data and ZLP send

 Same situation as on previous slide but now we send zero length packet on the end (length is 64)

```
/* USER CODE BEGIN 2 */
while(HAL_GPIO_ReadPin(GPIOA,GPIO_PIN_0)==GPIO_PIN_RESET){
}
while(count<5){
    if((USBD_CDC_HandleTypeDef*)(hUsbDeviceHS.pClassData))->TxState==0){
        if(CDC_Transmit_HS(buffer,length)==USBD_OK){
            count++;
        }
    }
    hile(((USBD_CDC_HandleTypeDef*)(hUsbDeviceHS.pClassData))->TxState!=0)
    {
        DC_Transmit_HS(buffer,0);
        /* USER CODE_END 2 */
    }
}
```

Now windows terminal will receive data





USB VCP Host



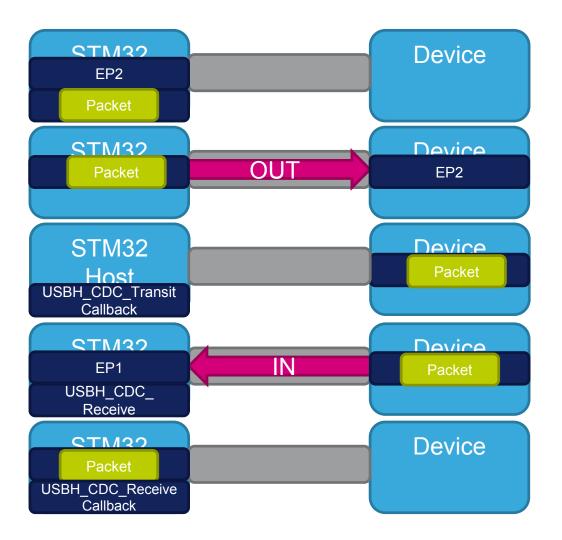
USB F4 VCP Host

- The CubeMX CDC host is very easy to handle
- There inly few function to handle
- Most important thing is function USBH Process which must be periodically called
- This function us periodically called from main.c in projects generated by CubeMX
- For sending data over CDC we use function USBH_CDC_Transmit
- And for reading data from device USBH_CDC_Receive
- USBH CDC TransmitCallback is weak call-back called when data was succesfouly transferred

USBH CDC ReceiveCallback is called when data was received

Cube VCP HOST Functionality 72

CDC HOST FLOW





- Create project in CubeMX
 - Menu > File > New Project
 - Select STM32F4 > STM32F429/439 > LQFP144 > STM32F439ZITx
- Select USB HS OTG internal PHY(FS)

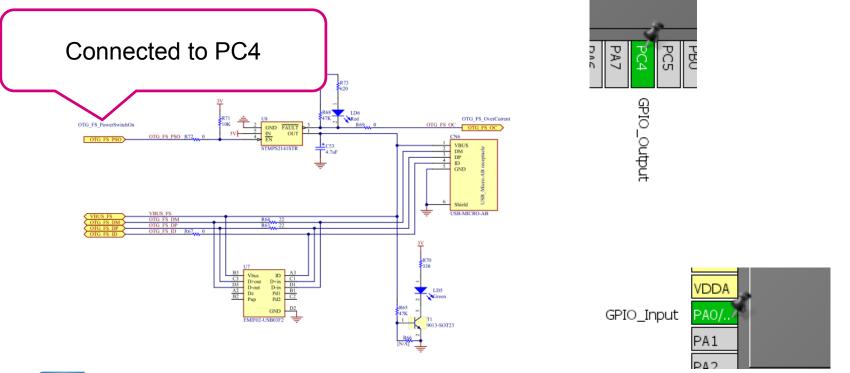
🖨 💿 USB_OTG_I	HS		222	
External Phy	Disable	+	PD8	
Internal Phy	Host_Only	•	PB15	USB_OTG_HS_DP
Activate	-		PB14	USB OTG HS DM
Acuvate	_1003		PB13	

Select HSE clock

• (HSI cannot be used and STM32F4 have no clock synchronization)

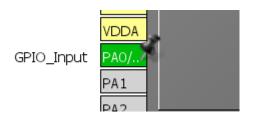
er 💿 RCC	Configuration
High Speed Clock (HSE) Crystal/Ceramic Resona 👻	i⊇MiddleWares
Low Speed Clock (LSE) Disable 🗸	E TATES
Master Clock Output 1	FREERTOS
····· Master Clock Output 2	
Audio Clock Input (I2S_CKIN)	
Select CDC class in MiddleWa	
_	Class for HS IP Communication Host Class (V
	Class for FS IP Disable

- Because HOST must also power the device we need to enable voltage regulator connected to VBUS line
- Regulator enable pin is on PC4(only select as output is enough because default state then will be LOW)





• We also enable PA0 where is button only for demo purpose



USB clock set to 48MHz and core clock at maximum



- In Configuration tab select USB_HS in Connectivity
- Disable option use internal DMA
- Button OK

Middlewares	
USB_OTG_HS Configuration	
Parameter Settings 🖌 NVIC Settings	Connectivity System
Configure the below parameters :	
l l l l l l l l l l l l l l l l l l l	
	GPIO ->>
Speed Full Speed 12MBit/s	
Enable internal IP DMA Disabled	
Physical interface Internal Phy	
	RCC 🔧 🥜
Apply Ok Cancel	



Now we set the project details for generation

- Menu > Project > Project Settings
- Set the project name
- Project location
- Type of toolchain
- Now we can Generate Code
 - Menu > Project > Generate Code
- If you have KEIL change HEAP size in startup file

Project Code Generator Project Settings Project Name VCP_HOST Project Location D:\Radek_Training_examples\F4_USB\Jabs Ditered to the set of the set	X
Project Name VCP_HOST Project Location D:\Radek\Training_examples\F4_USB\\abs Toolchain Folder Location D:\Radek\Training_examples\F4_USB\\abs\VCP_HOST\ Toolchain / IDE EWARM 6.70 Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
VCP_HOST Project Location D:\Radek\Training_examples\F4_USB\labs Toolchain Folder Location D:\Radek\Training_examples\F4_USB\labs\VCP_HOST\ Toolchain / IDE EWARM 6.70 Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
Project Location D:\Radek_Training_examples\F4_USB\labs Toolchain Folder Location D:\Radek_Training_examples\F4_USB\labs\VCP_HOST\ Toolchain / IDE EWARM 6.70 Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
D:\Radek_Training_examples\F4_USB\labs Toolchain Folder Location D:\Radek_Training_examples\F4_USB\labs\VCP_HOST\ Toolchain / IDE EWARM 6.70 ✓ Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
Toolchain Folder Location D:\Radek_Training_examples\F4_USB\abs\VCP_HOST\ Toolchain / IDE EWARM 6.70 Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
D:\Radek\Training_examples\F4_USB\Jabs\VCP_HOST\ Toolchain / IDE EWARM 6.70 Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
Toolchain / IDE EWARM 6.70 Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
EWARM 6.70 Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
Mcu and Firmware Package Mcu Reference STM32F439ZITx Firmware Package Name and Version	
Mcu Reference STM32F439ZITx Firmware Package Name and Version	
Firmware Package Name and Version	
STM32Cube FW_F4 V1.4.0	
Ok	Cancel



 In main.c is additional function MX_USB_HOST_Process this function must be periodically called, if not USB Host will be not functional

```
/* USER CODE BEGIN 3 */
  /* Infinite loop */
while (1)
  {
    MX_USB_HOST_Process();
  }
  /* USER CODE END 3 */
```

 CubeMX generate is in infinite loop put I recommend you to handle it by interrupt or in RTOS put it into task



- In usb_host.c you may find callbacks from CDC
- USBH_UserProcess callback storing state of connected device into Appli_state variable
- If the Device is connected and enumerated into Appli_state is stored APPLICATION_READY and we can commutate with device

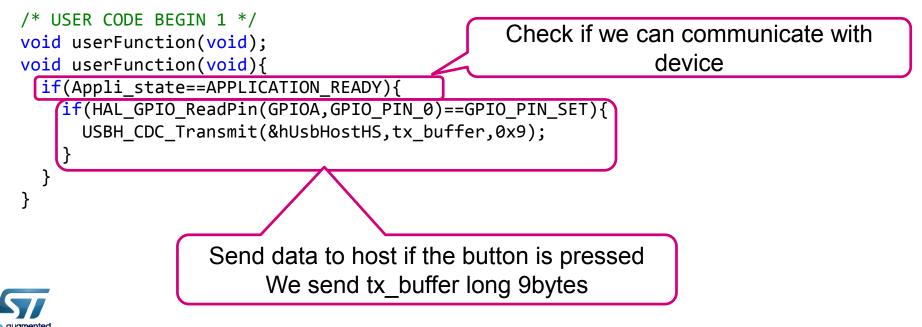
```
/*
 * user callbak definition
*/
static void USBH_UserProcess (USBH_HandleTypeDef *phost, uint8_t id)
{
 /* USER CODE BEGIN 2 */
 switch(id)
 {
                                                Device not connected
 case HOST USER SELECT CONFIGURATION:
 break;
 case HOST USER DISCONNECTION:
 Appli state = APPLICATION DISCONNECT;
 break;
 case HOST USER CLASS ACTIVE:
 Appli state = APPLICATION READY;
                                             Device can communicate
 break;
 case HOST USER CONNECTION:
 Appli state = APPLICATION START;
 break:
 default:
 break;
  /* USER CODE END 2 */
```

life.a

• In usb_host.c we define buffers for sending data and receiving

```
/* USER CODE BEGIN 0 */
uint8_t rx_buffer[100];
uint8_t tx_buffer[]="Hello\n";
/* USER CODE END 0 */
```

 In user section we define function which will send data into CDC device after button press



- In usb_host.c we also define two callbacks
- USBH_CDC_TransmitCallback which is called when data was successfully transmitted
- USBH_CDC_ReceiveCallback called if data was received

After data was transmitted to CD device we Request reading from CDC device

void USBH_CDC_TransmitCallback(USBH_HandleTypeDef *phost){
 USBH_CDC_Receive(phost,rx_buffer,0x9);

void USBH_CDC_ReceiveCallback(USBH_HandleTypeDef *phost){
 printf(rx_buffer);

/* USER CODE END 1 *

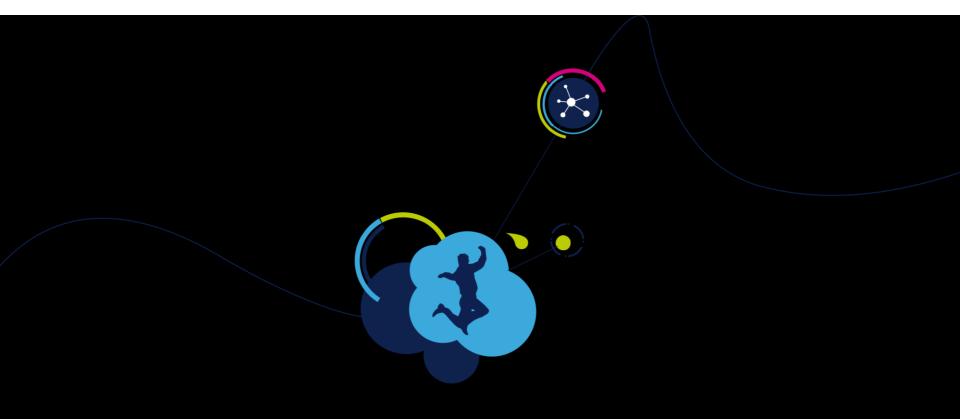
When data was read from device we print them to terminal(SWO)



- Now only thing what is missing is call userFunction which will send data after button press
- I put it into MX_USB_HOST_Process is not ideal because CubeMX can regenerate it but for demonstration purpose it is inapt

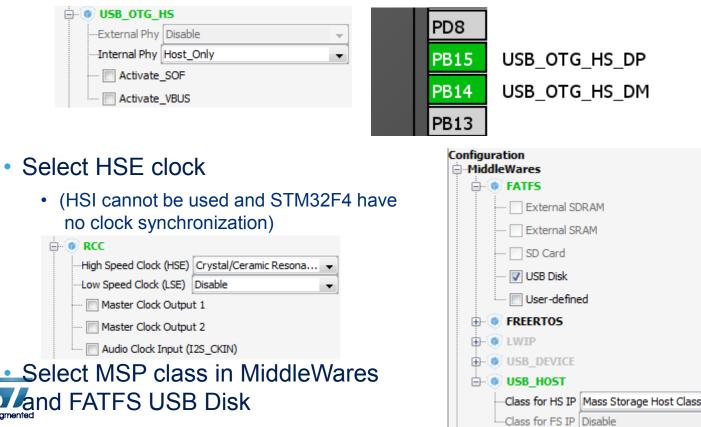
```
/*
 * Background task
*/
void MX_USB_HOST_Process()
{
   /* USB Host Background task */
   USBH_Process(&hUsbHostHS);
   userFunction();
}
```



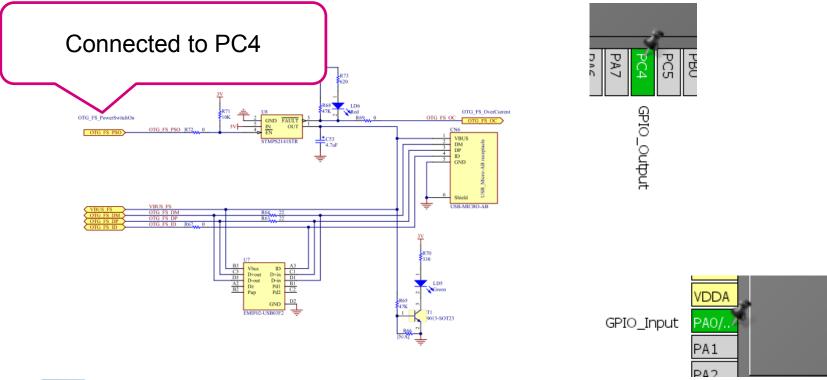




- Create project in CubeMX
 - Menu > File > New Project
 - Select STM32F4 > STM32F429/439 > LQFP144 > STM32F439ZITx
- Select USB HS OTG internal PHY(FS)

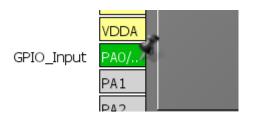


- Because HOST must also power the device we need to enable voltage regulator connected to VBUS line
- Regulator enable pin is on PC4(only select as output is enough because default state then will be LOW)





We also enable PA0 where is button only for demo purpose



USB clock set to 48MHz and core clock at maximum



- In Configuration tab select USB_HS in Connectivity
- Disable option use internal DMA
- Button OK

	Middlewares		
		_	
💿 US	3_OTG_HS Configuration		
V Pa	rameter Settings 🔣 NVIC Settings 🔣 GPIO Settings	Connectivity	System
Configu	re the below parameters :		
			GPIO -
	Speed Full Speed 12MBit/s		
	Enable internal IP DMA Disabled		
	Physical interface Internal Phy		
			RCC 🔧 🥜
		-	
	Apply Ok Cancel		



Now we set the project details for generation

- Menu > Project > Project Settings
- Set the project name
- Project location
- Type of toolchain
- Now we can Generate Code
 - Menu > Project > Generate Code
- If you have KEIL change HEAP size in startup file

Project Settings	3
Project Code Generator	
Project Settings	
Project Name	
MSP_HOST	
Project Location	
D:\Radek\Training_examples\F4_USB\abs	
Toolchain Folder Location	
D:\Radek\Training_examples\F4_USB\abs\MSP_HOST\	
Toolchain / IDE	
EWARM 6.70	
Mcu and Firmware Package Mcu Reference	
STM32F439ZITx	
Firmware Package Name and Version	
STM32Cube FW_F4 V1.4.0	
Ok Cancel	



- If the Device is connected and enumerated into appli_state is stored APPLICATION_READY and we can commutate with device
- For this reason we import into main.c appli_state variable

extern ApplicationTypeDef Appli_state;

We also need FATFS variable and FIL for file operations

```
/* USER CODE BEGIN PV */
extern ApplicationTypeDef Appli_state;
FIL fp; //file handle
FATFS fatfs; //structure with file system information
char text[]="test";//text which will be written into file
char name[]="test.txt";//name of the file
char text2[100];//buffer for data read from file
uint32_t ret;//return variable
/* USER CODE END PV */
```

Other variable are for lab purposes



• First we need mount the USB flash disk.

```
/* USER CODE BEGIN 3 */
   /* Initialises the File System*/
   if ( f_mount( &fatfs,"",0) != FR_OK )
        {
        /* fs initialisation fails*/
        while(1);
   }
```

 Please note that FLASH disk must be formatted in FAT32 file system otherwise is not possible to mount it



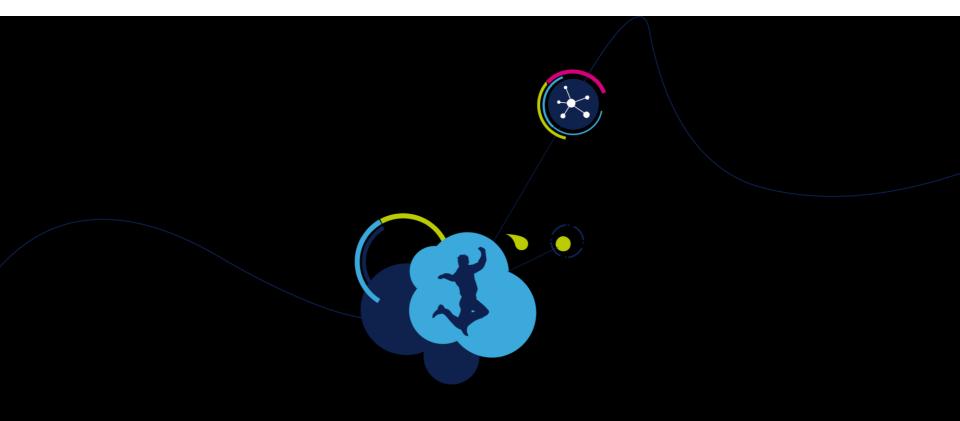
Basic operation with file system, reading and writing data from file "text.txt"

```
/* Infinite loop */
  while (1)
  {
    MX_USB_HOST_Process();
    if(Appli state==APPLICATION READY){
      /*open or create file for writing*/
      if(f_open(&fp,name,FA_CREATE_ALWAYS | FA_WRITE)!=FR_OK){
        while(1);
      }
      /*write data into flashdisk*/
      if(f_write(&fp,text,strlen(text),&ret)!=FR_OK){
        while(1);
      }
      f close(&fp);
      /*open file for reading*/
      if(f open(&fp,name,FA READ)!=FR OK){
        while(1);
      }
      /*red data from flash*/
      if(f_read(&fp,text2,100,&ret)!=FR_OK){
        while(1);
      }
      f close(&fp);
    }
  }
  /* USER CODE END 3 */
```



- From the past we know that some flash sticks can have problems with out library(STD)
- The USB MSP library is now only interface between flash drive and file system
- The basic operation which are done with MSP USB part is calling two BULK transfer one for READ BLOCK and second WRITE BLOCK



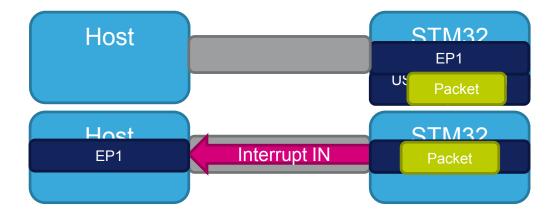


USB HID Device lab



USB HID Device lab 94

- HID device communicate over interrupt endpoint which guarantee the delivery in finite time
- In our CubeMX library is implemented the mouse report descriptor
- For change it you need to modify report descriptor first





- Create project in CubeMX
 - Menu > File > New Project
 - Select STM32F4 > STM32F429/439 > LQFP144 > STM32F439ZITx
- Select USB HS OTG internal PHY(FS)

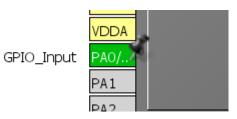


- Select HSE clock
 - (HSI cannot be used and STM32F4 have no clock synchronization)

i≑- 💿 RCC	
High Speed Clock (HSE)	Crystal/Ceramic Resona 👻
Low Speed Clock (LSE)	Disable
···· 🕅 Master Clock Outp	ut 1
···· 🔲 Master Clock Outp	ut 2
Audio Clock Input	I2S_CKIN)
 Select HID class 	s in MiddleWares



• We also enable PA0 where is button only for demo purpose



USB clock set to 48MHz and core clock at maximum



- In Configuration tab select USB_HS in Connectivity
- Disable option use internal DMA
- Button OK

Middlewares						
USB_OTG_	HS Configuration			x		
Parameter	Settings 🔣 NVIC Settings 刘 GPIC) Settings			Connectivity	System
	pelow parameters :				USB_HS	
	,					GPIO ->>
Spee	a oint 0 Max Packet size	Full Speed 12MBit/s 64 Bytes				
	le internal IP DMA	Disabled		וויר		NVIC
	ical interface	Internal Phy		┛╢║		
Low p	power	Disabled				RCC 🔦 🧹
Use d	dedicated end point 1 interrupt	Disabled				
VBUS	sensing	Disabled				
		Apply	Ok Cance			



Now we set the project details for generation

- Menu > Project > Project Settings
- Set the project name
- Project location
- Type of toolchain
- Now we can Generate Code
 - Menu > Project > Generate Code
- If you have KEIL change HEAP size in startup file

Project Settings
Project Code Generator
Project Settings
Project Name
USB_HID_Device
Project Location
D:\Radek\Training_examples\F4_USB\Jabs
Toolchain Folder Location
D:\Radek\Training_examples\F4_USB\Jabs\USB_HID_Device\
Toolchain / IDE
EWARM 6.70 👻
Mcu and Firmware Package Mcu Reference
STM32F439ZITx
Firmware Package Name and Version
STM32Cube FW_F4 V1.4.0
Ok Cancel



- The message which the HID device send have format defined in REPORT descriptor
- This format have only basic rules but descriptor for one device can look very different but functionality will be same
- Handling and parsing descriptors is on host
- Descriptor generated by CubeMX PC expects in this format:



 If you want to change format of this message you need to change the REPORT DESCRIPTOR in file usbd_hid.c the report descriptor array is called HID_MOUSE_ReportDesc



• We will work only in main.c

First include the USB handle

```
/* USER CODE BEGIN PV */
extern USBD_HandleTypeDef hUsbDeviceHS;
/* USER CODE END PV */
```

And include hid header file

```
/* USER CODE BEGIN Includes */
#include "usbd_hid.h"
/* USER CODE END Includes */
```

Define buffer which will be send to the host

```
/* USER CODE BEGIN PFP */
uint8_t buffer[4];
/* USER CODE END PFP */
```



- USBD_HID_SendReport will send the buffer on button press
- The buffer variable contains data about the mouse move and state of buttons
- With settings bellow, every button press move with cursor

```
/* USER CODE BEGIN 2 */
buffer[0]=0;//buttons first 3 bits
buffer[1]=100;//X axis 8bit value signed
buffer[2]=0;//Y axis 8bit value signed
buffer[3]=0;//Wheel 8bit value signed
 /* USER CODE END 2 */
/* USER CODE BEGIN 3 */
/* Infinite loop */
while (1)
 {
   if(HAL GPIO ReadPin(GPIOA,GPIO PIN 0)==GPIO PIN SET){
     USBD HID SendReport(&hUsbDeviceHS,buffer,4);
     HAL Delay(100);
   USER CODE END 3 */
```

- In CubeMX change PID to 22316
- And regenerate code

USB_DEVICE Configuration	×
Parameter Settings 🗹 Device Descriptor	
Configure the below parameters :	
Device Descriptor	
VID (Vendor IDentifier)	1155
LANGID_STRING (Language Identifier)	English(United States)
MANUFACTURER_STRING (Manufacturer Identifier)	STMicroelectronics
Device Descriptor HS	
PID (Product IDentifier)	22316
PRODUCT_STRING (Product Identifier)	STM32 Human interface
SERIALNUMBER_STRING (Serial number)	0000000001A
CONFIGURATION_STRING (Configuration Identifier)	HID Config
INTERFACE_STRING (Interface Identifier)	HID Interface
	Apply Ok Cancel



- In usbd_hid.h
- Change size of report descriptor to 187

#define HID_MOUSE_REPORT_DESC_SIZE 187

• In usbd_hid.c change the protocol interface to keyboard



• Change report descriptor to(1):

__ALIGN_BEGIN static uint8_t HID_MOUSE_ReportDesc[HID_MOUSE_REPORT_DESC_SIZE] __ALIGN_END = {

0x05	,//bSize: 0x01, bType: Global, bTag: Usage Page
0x01	<pre>,//Usage Page(Generic Desktop Controls)</pre>
0x09	,//bSize: 0x01, bType: Local, bTag: Usage
0x06	,//Usage(Keyboard)
0xA1	<pre>,//bSize: 0x01, bType: Main, bTag: Collection</pre>
0x01	,//Collection(Application)
0x85	,//bSize: 0x01, bType: Global, bTag: Report ID
0x01	,//Report ID(0x1)
0x05	,//bSize: 0x01, bType: Global, bTag: Usage Page
0x07	<pre>,//Usage Page(Keyboard/Keypad)</pre>
0x19	<pre>,//bSize: 0x01, bType: Local, bTag: Usage Minimum</pre>
0xE0	,//Usage Minimum(0xE0)
0x29	<pre>,//bSize: 0x01, bType: Local, bTag: Usage Maximum</pre>
0xE7	,//Usage Maximum(0xE7)
0x15	<pre>,//bSize: 0x01, bType: Global, bTag: Logical Minimum</pre>
0x00	,//Logical Minimum(0x0)
0x25	<pre>,//bSize: 0x01, bType: Global, bTag: Logical Maximum</pre>
0x01	<pre>,//Logical Maximum(0x1)</pre>
0x75	,//bSize: 0x01, bType: Global, bTag: Report Size
0x01	,//Report Size(0x1)



Change report descriptor to(2):

- 0x95 ,//bSize: 0x01, bType: Global, bTag: Report Count
- 0x08 ,//Report Count(0x8)
- 0x81 ,//bSize: 0x01, bType: Main, bTag: Input
- 0x02 ,//Input(Data, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Bit Field)
- 0x75 ,//bSize: 0x01, bType: Global, bTag: Report Size
- 0x08 ,//Report Size(0x8)
- 0x95 ,//bSize: 0x01, bType: Global, bTag: Report Count
- 0x01 ,//Report Count(0x1)
- 0x81 ,//bSize: 0x01, bType: Main, bTag: Input
- 0x01 ,//Input(Constant, Array, Absolute, No Wrap, Linear, Preferred State, No Null Position, Bit Field)
- 0x05 ,//bSize: 0x01, bType: Global, bTag: Usage Page
- 0x07 ,//Usage Page(Keyboard/Keypad)
- 0x19 ,//bSize: 0x01, bType: Local, bTag: Usage Minimum
- 0x00 ,//Usage Minimum(0x0)
- 0x29 ,//bSize: 0x01, bType: Local, bTag: Usage Maximum
- 0x65 ,//Usage Maximum(0x65)
- 0x15 ,//bSize: 0x01, bType: Global, bTag: Logical Minimum
- 0x00 ,//Logical Minimum(0x0)
- 0x25 ,//bSize: 0x01, bType: Global, bTag: Logical Maximum
- 0x65 ,//Logical Maximum(0x65)
- 0x75 ,//bSize: 0x01, bType: Global, bTag: Report Size



Change report descriptor to(3):

```
0x08
        ,//Report Size(0x8 )
0x95
        ,//bSize: 0x01, bType: Global, bTag: Report Count
        ,//Report Count(0x5 )
0x05
        ,//bSize: 0x01, bType: Main, bTag: Input
0x81
0x00
        ,//Input(Data, Array, Absolute, No Wrap, Linear, Preferred State, No Null Position, Bit Field)
        ,//bSize: 0x00, bType: Main, bTag: End Collection
0xC0
0x05
        ,//bSize: 0x01, bType: Global, bTag: Usage Page
        ,//Usage Page(Consumer )
0x0C
        ,//bSize: 0x01, bType: Local, bTag: Usage
0x09
        ,//Usage(Consumer Control)
0x01
        ,//bSize: 0x01, bType: Main, bTag: Collection
0xA1
0x01
        ,//Collection(Application )
        ,//bSize: 0x01, bType: Global, bTag: Report ID
0x85
        ,//Report ID(0x2 )
0x02
        ,//bSize: 0x01, bType: Local, bTag: Usage Minimum
0x19
        ,//Usage Minimum(0x0 )
0x00
0x2A
        ,//bSize: 0x02, bType: Local, bTag: Usage Maximum
0x3C,
0x02,//3C
             ,//Usage Maximum(0x23C )
        ,//bSize: 0x01, bType: Global, bTag: Logical Minimum
0x15
        ,//Logical Minimum(0x0 )
0x00
0x26
        ,//bSize: 0x02, bType: Global, bTag: Logical Maximum
```



Change report descriptor to(4):

```
0x3C,
0x02,//3C
             ,//Logical Maximum(0x23C )
0x95
        ,//bSize: 0x01, bType: Global, bTag: Report Count
0x01
        ,//Report Count(0x1 )
        ,//bSize: 0x01, bType: Global, bTag: Report Size
0x75
        ,//Report Size(0x10 )
0x10
0x81
        ,//bSize: 0x01, bType: Main, bTag: Input
        ,//Input(Data, Array, Absolute, No Wrap, Linear, Preferred State, No Null Position, Bit Field)
0x00
        ,//bSize: 0x00, bType: Main, bTag: End Collection
0xC0
        ,//bSize: 0x01, bType: Global, bTag: Usage Page
0x05
        ,//Usage Page(Generic Desktop Controls )
0x01
0x09
        ,//bSize: 0x01, bType: Local, bTag: Usage
        ,//Usage(System Control)
0x80
        ,//bSize: 0x01, bType: Main, bTag: Collection
0xA1
        ,//Collection(Application )
0x01
        ,//bSize: 0x01, bType: Global, bTag: Report ID
0x85
0x03
        ,//Report ID(0x3 )
        ,//bSize: 0x01, bType: Local, bTag: Usage Minimum
0x19
        ,//Usage Minimum(0x81 )
0x81
        ,//bSize: 0x01, bType: Local, bTag: Usage Maximum
0x29
        ,//Usage Maximum(0x83 )
0x83
0x15
        ,//bSize: 0x01, bType: Global, bTag: Logical Minimum
```



Change report descriptor to(5):

,//Logical Minimum(0x0) 0x00 ,//bSize: 0x01, bType: Global, bTag: Logical Maximum 0x25 0x01 ,//Logical Maximum(0x1) ,//bSize: 0x01, bType: Global, bTag: Report Size 0x75 0x01 ,//Report Size(0x1) ,//bSize: 0x01, bType: Global, bTag: Report Count 0x95 0x03 ,//Report Count(0x3) ,//bSize: 0x01, bType: Main, bTag: Input 0x81 ,//Input(Data, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Bit Field) 0x02 ,//bSize: 0x01, bType: Global, bTag: Report Count 0x95 ,//Report Count(0x5) 0x05 0x81 ,//bSize: 0x01, bType: Main, bTag: Input ,//Input(Constant, Array, Absolute, No Wrap, Linear, Preferred State, No Null Position, Bit Field) 0x01 ,//bSize: 0x00, bType: Main, bTag: End Collection 0xC0 ,//bSize: 0x02, bType: Global, bTag: Usage Page 0x06 0x01, 0xFF, //01 ,//Usage Page(Undefined) ,//bSize: 0x01, bType: Local, bTag: Usage 0x09 0x01 ,//Usage(1) ,//bSize: 0x01, bType: Main, bTag: Collection 0xA1 ,//Collection(Application) 0x01



Change report descriptor to(6):

,//bSize: 0x01, bType: Global, bTag: Report ID

0x85

```
0x04
             ,//Report ID(0x4 )
             ,//bSize: 0x01, bType: Global, bTag: Report Count
     0x95
             ,//Report Count(0x1 )
     0x01
     0x75
             ,//bSize: 0x01, bType: Global, bTag: Report Size
     0x08
             ,//Report Size(0x8 )
     0x15
             ,//bSize: 0x01, bType: Global, bTag: Logical Minimum
             ,//Logical Minimum(0x1 )
     0x01
             ,//bSize: 0x01, bType: Global, bTag: Logical Maximum
     0x25
             ,//Logical Maximum(0xA )
     0x0A
             ,//bSize: 0x01, bType: Local, bTag: Usage
     0x09
     0x20
             ,//Usage(32)
     0xB1
             ,//bSize: 0x01, bType: Main, bTag: Feature
             ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non
     0x03
VolatileBit Field)
     0x09
             ,//bSize: 0x01, bType: Local, bTag: Usage
             ,//Usage(35)
     0x23
             ,//bSize: 0x01, bType: Main, bTag: Feature
     0xB1
             ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non
     0x03
VolatileBit Field)
     0x25
             ,//bSize: 0x01, bType: Global, bTag: Logical Maximum
     0x4F
             ,//Logical Maximum(0x4F )
             ,//bSize: 0x01, bType: Local, bTag: Usage
     0x09
```



Change report descriptor to(7):

0x21 ,//Usage(33) 0xB1 ,//bSize: 0x01, bType: Main, bTag: Feature ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non 0x03 VolatileBit Field) 0x25 ,//bSize: 0x01, bType: Global, bTag: Logical Maximum 0x30 ,//Logical Maximum(0x30) 0x09 ,//bSize: 0x01, bType: Local, bTag: Usage ,//Usage(34) 0x22 ,//bSize: 0x01, bType: Main, bTag: Feature 0xB1 ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non 0x03 VolatileBit Field) 0x95 ,//bSize: 0x01, bType: Global, bTag: Report Count ,//Report Count(0x3) 0x03 ,//bSize: 0x01, bType: Local, bTag: Usage 0x09 0x24 ,//Usage(36) 0xB1 ,//bSize: 0x01, bType: Main, bTag: Feature ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non 0x03 VolatileBit Field) 0xC0 ,//bSize: 0x00, bType: Main, bTag: End Collection ,//bSize: 0x02, bType: Global, bTag: Usage Page 0x06 0x01, 0xFF,//01 ,//Usage Page(Undefined) ,//bSize: 0x01, bType: Local, bTag: Usage 0x09



Change report descriptor to(8):

```
0x01
             ,//Usage(1)
     0xA1
             ,//bSize: 0x01, bType: Main, bTag: Collection
             ,//Collection(Application )
     0x01
     0x85
             ,//bSize: 0x01, bType: Global, bTag: Report ID
     0x05
             ,//Report ID(0x5)
             ,//bSize: 0x01, bType: Global, bTag: Report Count
     0x95
     0x01
             ,//Report Count(0x1 )
             ,//bSize: 0x01, bType: Global, bTag: Report Size
     0x75
     0x08
             ,//Report Size(0x8 )
             ,//bSize: 0x01, bType: Global, bTag: Logical Minimum
     0x15
             ,//Logical Minimum(0x1 )
     0x01
     0x25
             ,//bSize: 0x01, bType: Global, bTag: Logical Maximum
             ,//Logical Maximum(0xA )
     0x0A
             ,//bSize: 0x01, bType: Local, bTag: Usage
     0x09
     0x20
             ,//Usage(32)
     0xB1
             ,//bSize: 0x01, bType: Main, bTag: Feature
             ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non
     0x03
VolatileBit Field)
     0x09
             ,//bSize: 0x01, bType: Local, bTag: Usage
     0x23
             ,//Usage(35)
             ,//bSize: 0x01, bType: Main, bTag: Feature
     0xB1
     0x03
             ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non
VolatileBit Field)
```



Change report descriptor to(9):

,//bSize: 0x01, bType: Global, bTag: Logical Maximum 0x25 0x4F ,//Logical Maximum(0x4F) ,//bSize: 0x01, bType: Local, bTag: Usage 0x09 0x21 ,//Usage(33) 0xB1 ,//bSize: 0x01, bType: Main, bTag: Feature ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non 0x03 VolatileBit Field) ,//bSize: 0x01, bType: Global, bTag: Logical Maximum 0x25 0x30 ,//Logical Maximum(0x30) ,//bSize: 0x01, bType: Local, bTag: Usage 0x09 0x22 ,//Usage(34) 0xB1 ,//bSize: 0x01, bType: Main, bTag: Feature ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non 0x03 VolatileBit Field) 0x95 ,//bSize: 0x01, bType: Global, bTag: Report Count ,//Report Count(0x3) 0x03 0x09 ,//bSize: 0x01, bType: Local, bTag: Usage 0x24 ,//Usage(36) 0xB1 ,//bSize: 0x01, bType: Main, bTag: Feature ,//Feature(Constant, Variable, Absolute, No Wrap, Linear, Preferred State, No Null Position, Non 0x03 VolatileBit Field) 0xC0 ,//bSize: 0x00, bType: Main, bTag: End Collection

};



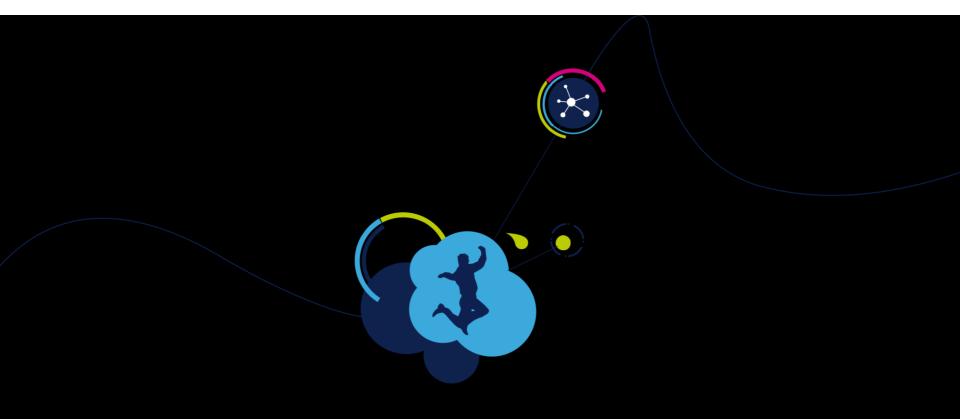
```
    In main change buffer size:
```

```
/* USER CODE BEGIN PFP */
uint8_t buffer[8];
/* USER CODE END PFP */
```

```
/* USER CODE BEGIN 2 */
 buffer[0]=1;//reportID
 buffer[1]=0;//modifier
 buffer[2]=0;//OEM
 buffer[3]=0x4E;//keycode data - PgDwn
 buffer[4]=0;//keycode data
 buffer[5]=0;//keycode data
 buffer[6]=0;//keycode data
 buffer[7]=0;//keycode data
 /* USER CODE END 2 */
 /* USER CODE BEGIN 3 */
 /* Infinite loop */
while (1)
 {
   if(HAL GPIO ReadPin(GPIOA,GPIO PIN 0)==GPIO PIN SET)
   {
     buffer[3]=0x4E;//keycode data - PgDwn press
     USBD HID SendReport(&hUsbDeviceHS,buffer,8);
     HAL Delay(100);
     buffer[3]=0x0;//keycode data - PgDwn release
     USBD_HID_SendReport(&hUsbDeviceHS,buffer,8);
     HAL Delay(100);
```

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}





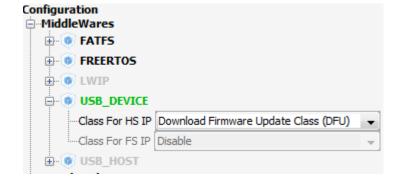
- Create project in CubeMX
 - Menu > File > New Project
 - Select STM32F4 > STM32F429/439 > LQFP144 > STM32F439ZITx
- Select USB HS OTG internal PHY(FS)



Select HSE clock

• (HSI cannot be used and STM32F4 have no clock synchronization)

	È- 💿 RCC		
	High Speed Clock (HSE)	Crystal/Ceramic Resona	•
	-Low Speed Clock (LSE)	Disable	•]
	····· 🥅 Master Clock Outpu	t 1	
	🔲 Master Clock Outpu	t 2	
	🔲 Audio Clock Input (1	2S_CKIN)	
• Se	elect HID class	s in MiddleW	ares





- In Configuration tab select USB_HS in Connectivity
- Disable option use internal DMA
- Button OK

Middlewares						
USB_OTG_HS Config	guration			x		
Parameter Settings		D Settings			Connectivity	System
Configure the below para	meters :					
Speed		Full Speed 12MBit/s				GPIO ->>
Endpoint 0 Max	Packet size	64 Bytes				
Enable internal		Disabled				
Physical interfa	ce	Internal Phy		_		RCC 🔦 🧹
Low power		Disabled				
Use dedicated	end point 1 interrupt	Disabled				
VBUS sensing		Disabled				
Apply Ok Cancel						



- In Configuration tab select USB_DEVICE in Middleware's
- Enable user string descriptor support
- Button OK

	Middlewares			
(USB_DEVICE Configuration			
Multime	Parameter Settings & Device Descriptor	stem		
	Configure the below parameters :	! +		
	Basic Parameters			
	USBD_MAX_NUM_INTERFACES (Maximum number of 1			
	USBD_MAX_NUM_CONFIGURATION (Maximum numb 1			
	USBD_MAX_STR_DESC_STZ (Maximum size for the str 512 bytes			
	USBD_SUPPORT_USER_STRING (Enable user string d Enabled	2		
	USBD_SELF_POWERED (Enabled self power) Enabled			
	USBD_DEBUG_LEVEL (USBD Debug Level) 0: No debug message			
	Class Parameters			
	USBD_DFU_MAX_ITF_NUM (DFU maximum interface 1			
	USBD_DFU_XFER_SIZE 1024 Bytes			
	USBD_DFU_APP_DEFAULT_ADD (Base Address 0x) 0x08000000			



Now we set the project details for generation

- Menu > Project > Project Settings
- Set the project name
- Project location
- Type of toolchain
- Now we can Generate Code
 - Menu > Project > Generate Code
- If you have KEIL change HEAP size in startup file

Project Settings
Project Code Generator
Project Settings
Project Name
USB_HID_Device
Project Location
D:\Radek\Training_examples\F4_USB\Jabs
Toolchain Folder Location
D:\Radek\Training_examples\F4_USB\labs\USB_HID_Device\
Toolchain / IDE
EWARM 6.70
Mcu and Firmware Package Mcu Reference
STM32F439ZITx
Firmware Package Name and Version
STM32Cube FW_F4 V1.4.0
Ok Cancel



- CubeMX create for us file usbd_dfu.c
- This file handling reading and writing into memory
- MEM_If_Init_HS
 - Initialize programing, called on programing start
- MEM_If_DeInit_HS
 - Deinitialize programing, called on programing end
- MEM_lf_Erase_HS
 - Erase selected part of memory
- MEM_If_Write_HS
 - Write into selected memory



- Read from selected memory
- MEM_If_GetStatus_HS
 - Return state of programing
 - Busy or ready

- We need to modify the usbd_dfu_it.c file
- We ned to change the string description of memory:

```
__ALIGN_BEGIN_USBD_DFU_MediaTypeDef_USBD_DFU_fops_HS __ALIGN_END =
{
```

```
(uint8_t *) "@Internal Flash /0x20020000/1*016Kg",
MEM_If_Init_HS,
MEM_If_DeInit_HS,
MEM_If_Erase_HS,
MEM_If_Write_HS,
MEM_If_Read_HS,
MEM_If_GetStatus_HS,
};
```

 Now the DFU tool will be able recognize that we can program RAM memory on address 0x20020000 and size of this memory is 16kB



 MEM_If_Init_HS and MEM_If_DeInit_HS function can be empty because we want program RAM which it to necessary to lock or unlock

```
uint16_t MEM_If_Init_HS(void)
{
    /* USER CODE BEGIN 7 */
    return (USBD_OK);
    /* USER CODE END 7 */
}
uint16_t MEM_If_DeInit_HS(void)
{
    /* USER CODE BEGIN 8 */
    return (USBD_OK);
    /* USER CODE END 8 */
}
```



• MEM_If_Erase_HS function simply set our RAM memory space to zero

```
uint16_t MEM_If_Erase_HS(uint32_t Add)
{
    /* USER CODE BEGIN 9 */
    uint32_t i;
    for(i=0;i<0x3FFF;i=i+4){
        *(uint32_t*)(0x20020000+i)=0;
    }
    return (USBD_OK);
    /* USER CODE END 9 */
}</pre>
```



MEM_If_Write_HS program the source buffer to destination buffer

```
uint16 t MEM If Write HS(uint8 t *src, uint8 t *dest, uint32 t Len)
{
 /* USER CODE BEGIN 10 */
 uint32 t i = 0;
 for(i = 0; i < Len; i+=4)</pre>
  {
     *(uint32_t*)(dest+i)=*(uint32_t*)(src+i);
    /* Check the written value */
    if(*(uint32 t *)(src + i) != *(uint32 t*)(dest+i))
    {
       return USBD FAIL;
    }
  }
 return (USBD OK);
  /* USER CODE END 10 */
}
```



 MEM_If_Read_HS read data from source address and copy it into destination address

```
uint8_t *MEM_If_Read_HS (uint8_t *src, uint8_t *dest, uint32_t Len)
{
    /* Return a valid address to avoid HardFault */
    /* USER CODE BEGIN 11 */
    uint32_t i = 0;
    uint8_t *psrc = src;
    for(i = 0; i < Len; i++)
    {
        dest[i] = *psrc++;
     }
    /* Return a valid address to avoid HardFault */
    return (uint8_t*)(dest);
    /* USER CODE END 11 */
}</pre>
```



 MEM_If_GetStatus_HS read information how long take programing and erasing

```
uint16_t MEM_If_GetStatus_HS (uint32_t Add, uint8_t Cmd, uint8_t *buffer)
{
  /* USER CODE BEGIN 12 */
#define MEMORY_ERASE_TIME (uint16_t)50
#define MEMORY PROGRAM TIME (uint16 t)50
  switch (Cmd)
  {
  case DFU MEDIA PROGRAM:
    buffer[1] = (uint8 t)MEMORY_PROGRAM_TIME;
    buffer[2] = (uint8 t)(MEMORY PROGRAM TIME << 8);</pre>
    buffer[3] = 0;
    break;
  case DFU MEDIA ERASE:
  default:
    buffer[1] = (uint8 t)MEMORY ERASE TIME;
    buffer[2] = (uint8 t)(MEMORY ERASE TIME << 8);</pre>
    buffer[3] = 0;
    break;
  }
  return (USBD OK);
  /* USER CODE END 12 */
```

life.c

• We can use DfuSe Demo to try program the selected memory

Maj

ĺ	OfuSe Demo (v3.0.3)
	Available DFU Devices STM Device in DFU Mode Supports Upload Manifestation tolerant Supports Download Accelerated Upload (ST) Can Detach Procuct ID: Enter DFU mode/HID detach Leave DFU mode Actions Select I arget(s): Target Id Name Available Sectors (Double Click for more) 00 Internal Flash
Sector Num Start Address End Address Size Readable Writea Sector 000 0x20020000 0x20023FFF 16 Kb X X	X Version: V
ote for Type: (R)eadable, (W)riteable, (E)rasable	

• We can use DfuSe Demo to try program the selected memory

Maj

ĺ	DfuSe Demo (v3.0.3)
	Available DFU Devices STM Device in DFU Mode Supports Upload Manifestation tolerant Supports Download Accelerated Upload (ST) Can Detach Enter DFU mode/HID detach Leave DFU mode Actions Select Larget(s): Target Id Name Available Sectors (Double Click for more) 00 Internal Flash
Sector Num Start Address End Address Size Readable Writea Sector 000 0x20020000 0x20023FFF 16 Kb X X	X tes) Version: Version: Ve
ote for Type: (R)eadable, (W)riteable, (E)rasable	